

LPC845-BRK fejlesztői kártya és fejlesztő rendszer installálás és használat

(C) Benesóczky Zoltán 2023

Link: [LPC845 Breakout Board for LPC84x family MCUs | NXP Semiconductors](https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/general-purpose-mcus/lpc800-arm-cortex-m0-plus/lpc845-breakout-board-for-lpc84x-family-mcus:LPC845-BRK#design-resources)

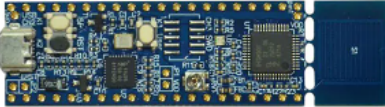
The screenshot displays the NXP website for the LPC845 Breakout Board. The page features a navigation bar with links to PRODUCTS, APPLICATIONS, DESIGN, SUPPORT, and COMPANY. A search bar and links for Sign In / Register and LANGUAGE are also present. The breadcrumb trail indicates the path: Home / Processors and Microcontrollers / Arm Microcontrollers / General Purpose MCUs / LPC800 Arm Cortex-M0+ / LPC845 Breakout Board.

LPC845 Breakout Board for LPC84x family MCUs

LPC845-BRK [Receive alerts](#)

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The LPC845 breakout board provides a powerful and flexible development system for NXP's low end Cortex-M0+-based LPC84x Family of MCUs, delivered in an ultra-low-cost evaluation board. This breakout board can be used with a range of development tools, including the MCUXpresso IDE toolchain. The LPCXpresso845 Breakout board is developed by NXP to enable evaluation of and prototyping with the LPC84x family of MCUs.

The LPC845 breakout board features an on-board CMSIS-DAP debug and VCOM port, RGB user LEDs, capacitive touch button, user potentiometer and allows an easy prototyping experience with access to 38 LPC845 port pins.

Roll over image to zoom in

DESIGN FILES **SOFTWARE**

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<https://www.nxp.com/products/processors-and-microcontrollers/arm-microcontrollers/general-purpose-mcus/lpc800-arm-cortex-m0-plus-/lpc845-breakout-board-for-lpc84x-family-mcus:LPC845-BRK#design-resources>

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LPC845 Breakout Board for LPC84x family MCUs

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Development Software

- ☐ BSP, Drivers and Middleware
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- ☐ IDE and Build Tools
- ☐ Test, Debug and Analyzer Software

IDE AND BUILD TOOLS

MCUXpresso Integrated Development Environment (IDE)

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Windows Type here to search

klukk: DOWN /mcuxpresso

https://www.nxp.com/design/software/development-software/mcuxpresso-software-and-tools-/mcuxpresso-integrated-development-environment-ide

MCUXpresso Integrated Development Environment (IDE) NXP

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IDE AND BUILD TOOLS

MCUXpresso Integrated Development Environment (IDE)

FLEXERA Rev All Jan 27, 2022 1 KB MCUXPRESSO

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Némi várakozás után:

The screenshot shows a web browser window displaying the NXP MCUXpresso IDE product information page. The browser's address bar shows the URL: <https://nxp.flexnetoperations.com/control/frse/product?entitlementId=688122587&lineNum=1&authContact...>. The page features the NXP logo and navigation links for PRODUCTS, APPLICATIONS, DESIGN, SUPPORT, and COMPANY. A search bar is located in the top right corner.

The main content area is titled "Product Information" and includes a sidebar with links to Software & Support, Licensing, and FAQ. The "Current" tab is selected, showing the "MCUXpresso IDE" product. Below the product name, there is a table with the following data:

Version	Description	Date Available	
11.7.0	MCUXpresso IDE	Jan 17, 2023	Download Log

The Windows taskbar at the bottom shows the system clock as 12:34 on 2023.02.17. The taskbar also includes icons for various applications, including the Start menu, search bar, and several open programs.

Klikk: MCUXpresso IDE

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Klikk: MCUXpresso IDE

Software & Support

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FAQ

- Download Help
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https://nxp.flexnetoperations.com/control/frse/download?element=13944367

Software Terms and Conditions

MCUXpresso IDE

Please read the following agreement and click "I AGREE" at the bottom before downloading your software.

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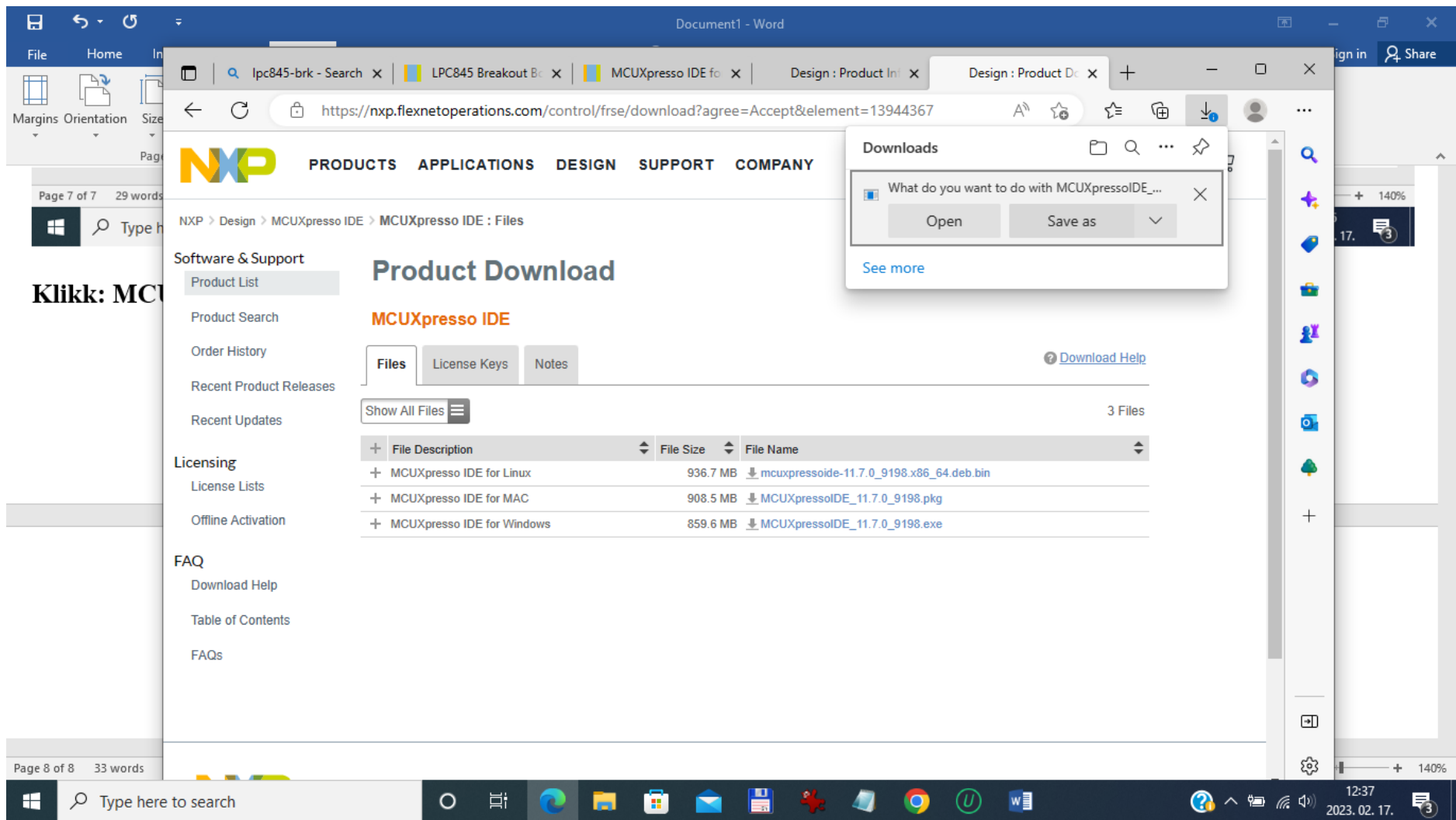
+	File Description	File Size	File Name
+	MCUXpresso IDE for Linux	936.7 MB	mcuxpressoide-11.7.0_9198.x86_64.deb.bin
+	MCUXpresso IDE for MAC	908.5 MB	MCUXpressoIDE_11.7.0_9198.pkg
+	MCUXpresso IDE for Windows	859.6 MB	MCUXpressoIDE_11.7.0_9198.exe

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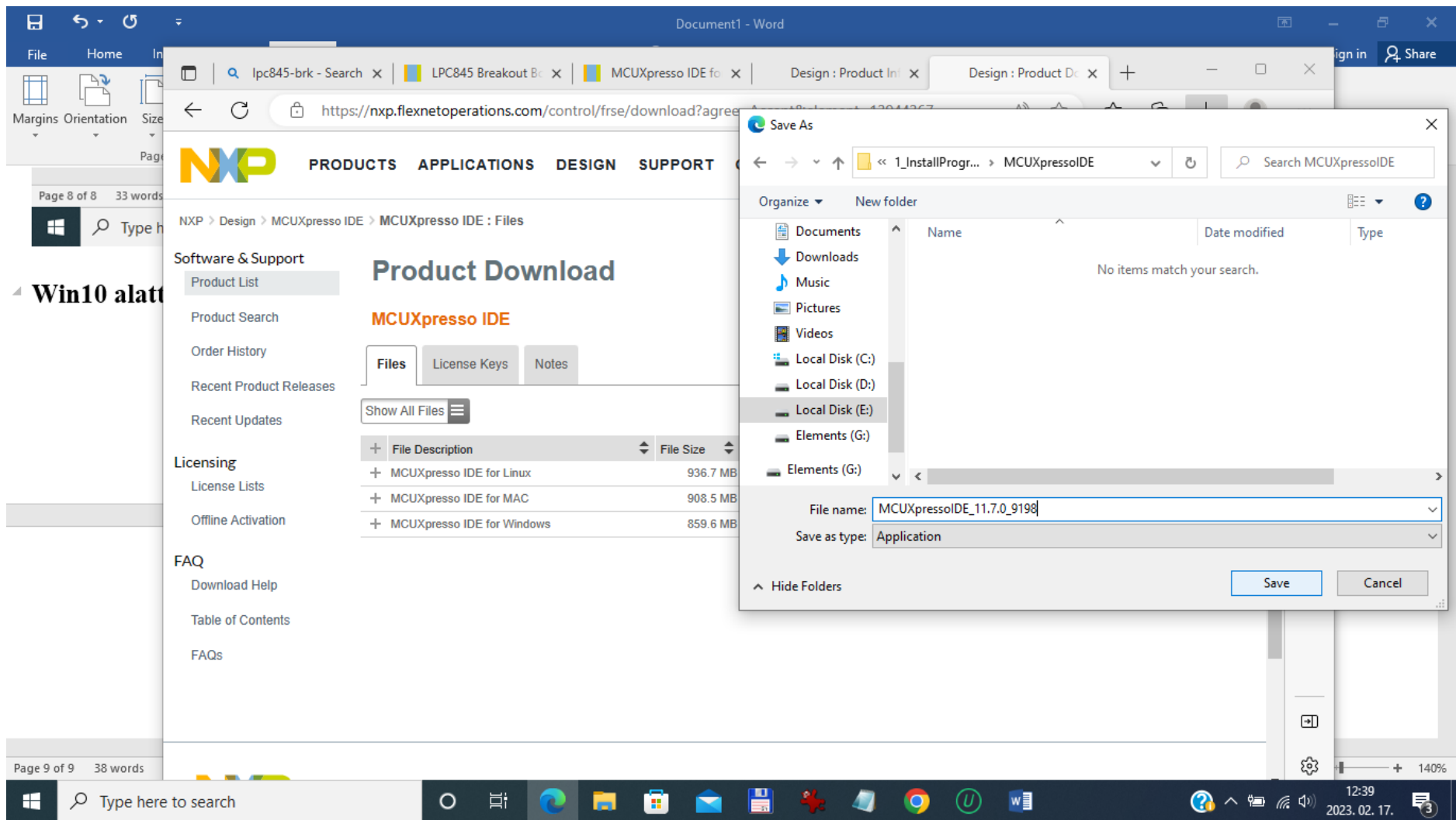
Type here to search

12:36 2023. 02. 17.

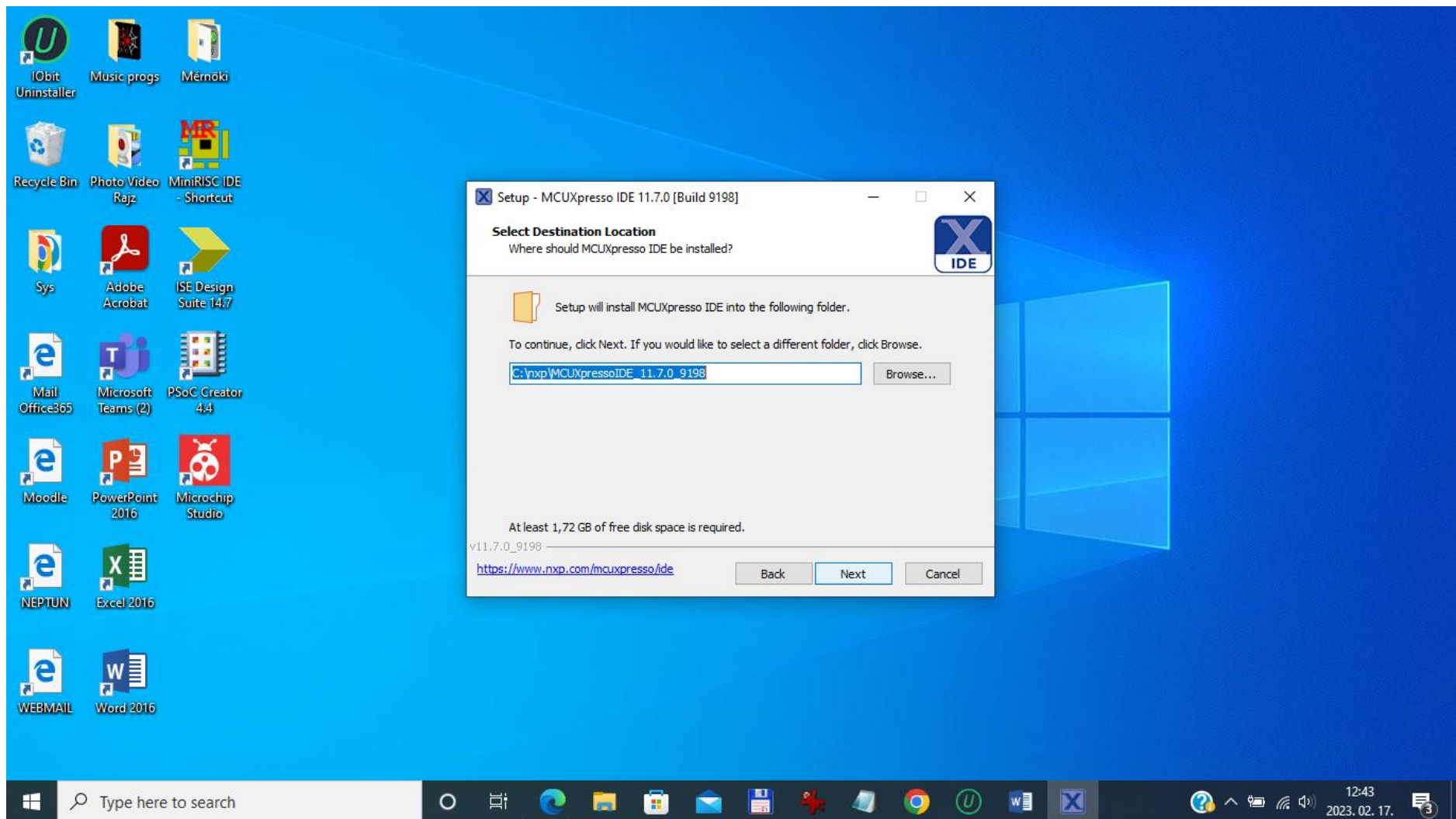
Klikk: MCUXpresso IDE Windows

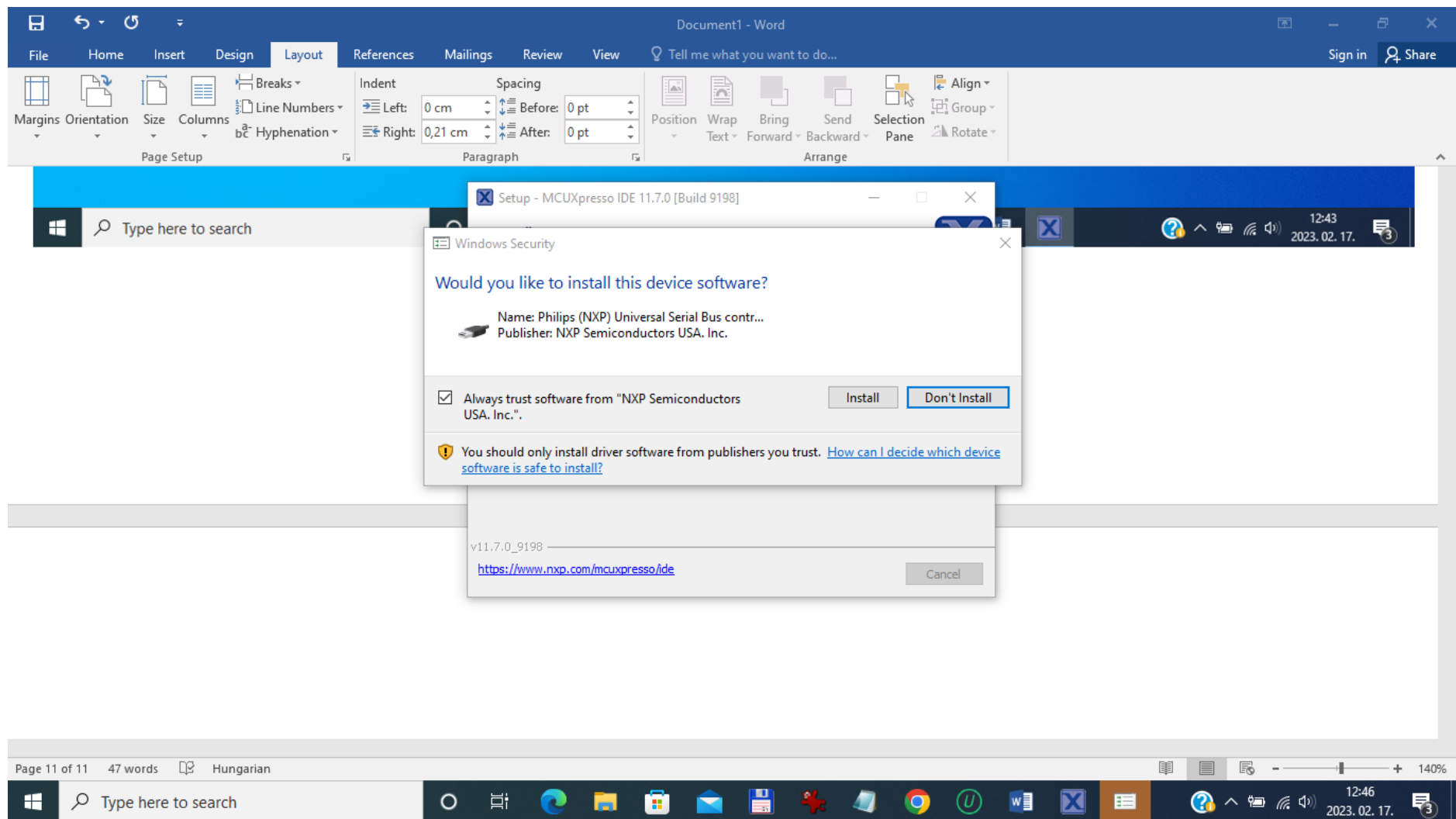


Win10 alatt Klikk: Save as

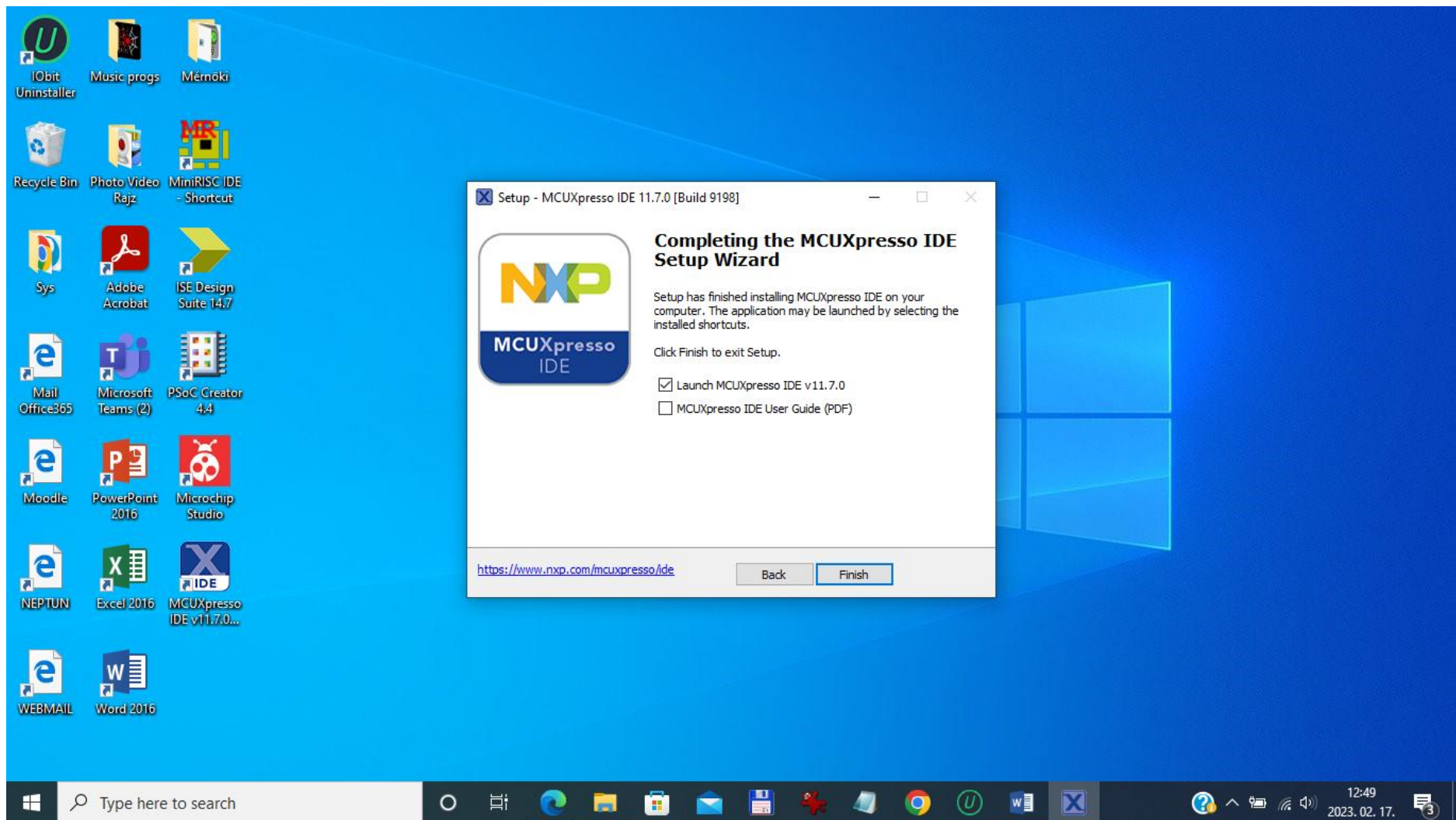


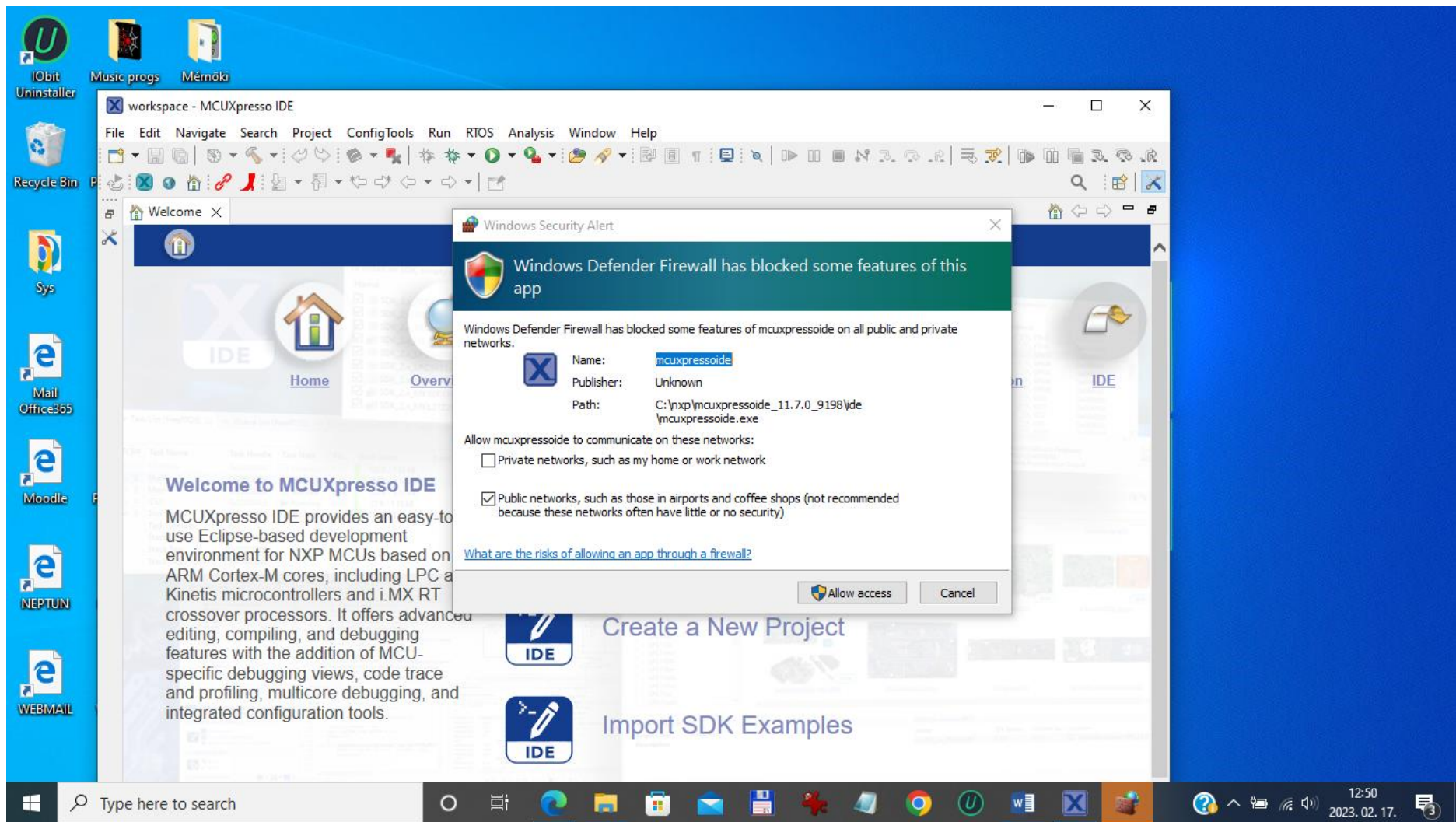
A célkönyvtár kiválasztása után mentés, accept agreement és installálás



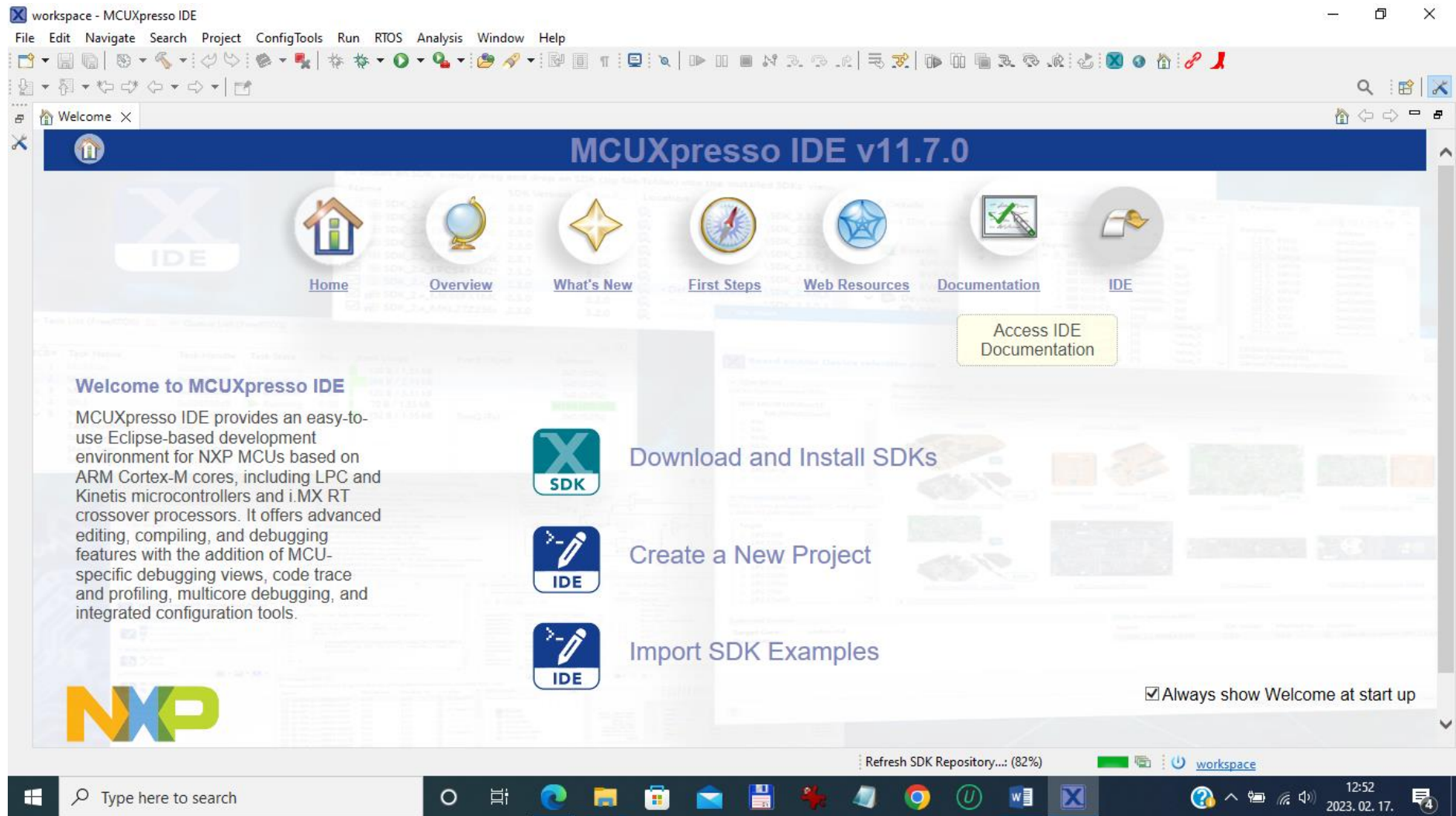


Installáljukunk mindent, amit kér...





Win10 alatt megint jöhet a Defender: Allow access



Várjuk meg a „Refresh SDK Repository”-t, utána katt: [Download and Install SDKs](#)

workspace - MCUXpresso IDE

File Edit Navigate Search Project ConfigTools Run RTOS Analysis Window Help

Install MCUXpresso SDKs Welcome

Select MCUXpresso SDKs to install from <https://mcuxpresso.nxp.com/eclipse/sdk>

SDKs add device support to MCUXpresso IDE allowing projects to be created and debugged.
Select and install one or more MCUXpresso SDKs to provide device knowledge, drivers, middleware, and reference example applications for your development board or MCU.

MCUXpresso Software and Tools

UNIFIED SUITE OF TOOLS FOR EASY DEVELOPMENT WITH NXP MCUs

LEARN MORE >

Boards Processors

Board	SDK	Version	Package	Flash	RAM	Status
evkbimxrt1050	SDK_2.x_EVKB-I...	2.13.0	MIMXRT1052D...	0	512	
evkbimxrt1050_om13790host	SDK_2.x_EVKB-I...	2.13.0	MIMXRT1052D...	0	512	
evkbmimxrt1060	SDK_2.x_MIMXRT...	2.13.0	MIMXRT1062D...	0	1024	
evkbmimxrt1060_om13790host	SDK_2.x_MIMXRT...	2.13.0	MIMXRT1062D...	0	1024	
evkmimxrt1010	SDK_2.x_EVK-MI...	2.13.0	MIMXRT1011D...	0	128	
evkmimxrt1015	SDK_2.x_EVK-MI...	2.13.0	MIMXRT1015D...	0	128	

Filter: type to filter MCU selection

☐ Hide Installed ☒ Show latest ☐ Hide board in

☐ External Flash

Cores

☐ Multicore

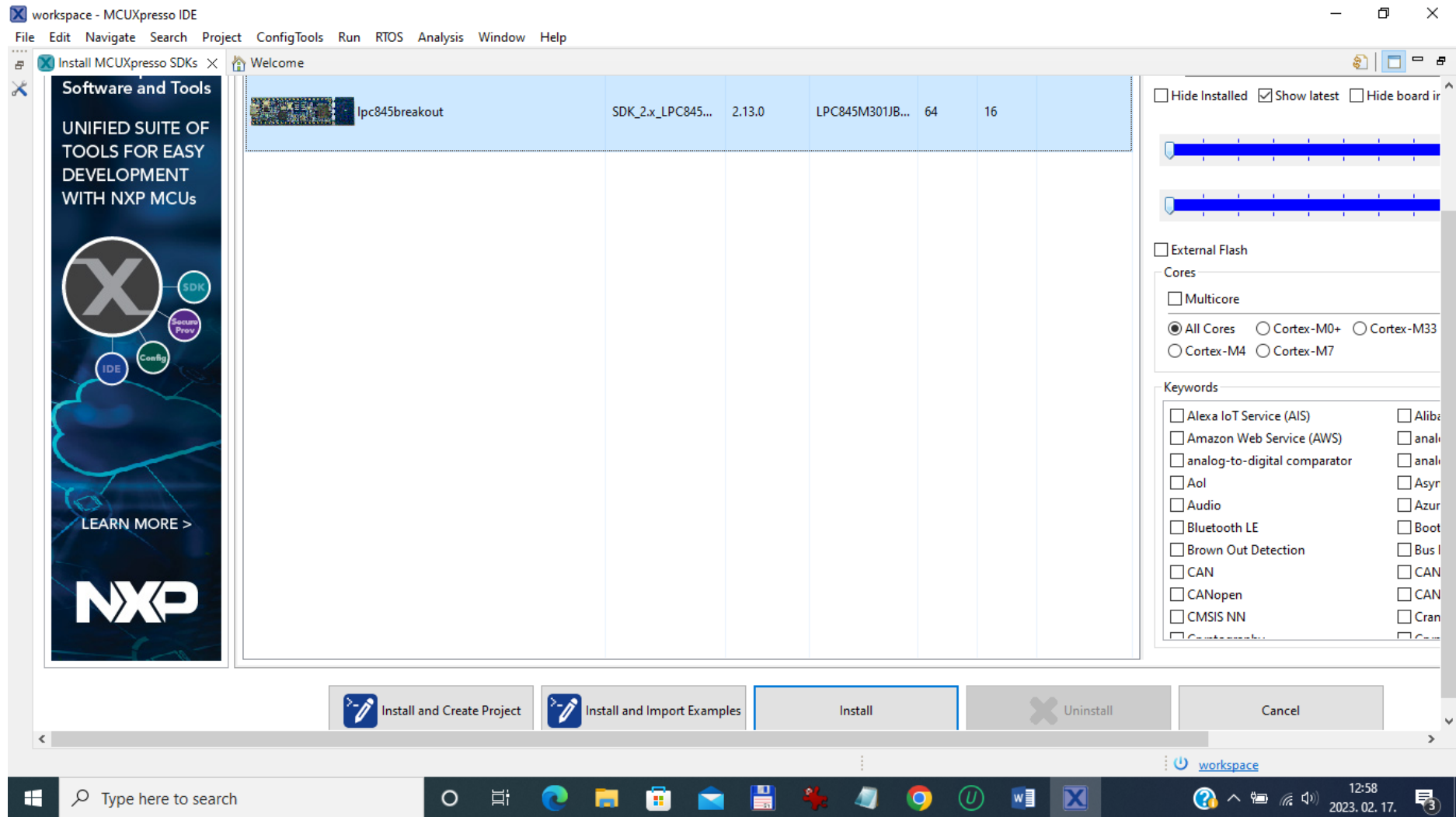
☒ All Cores ☐ Cortex-M0+ ☐ Cortex-M33

☐ Cortex-M4 ☐ Cortex-M7

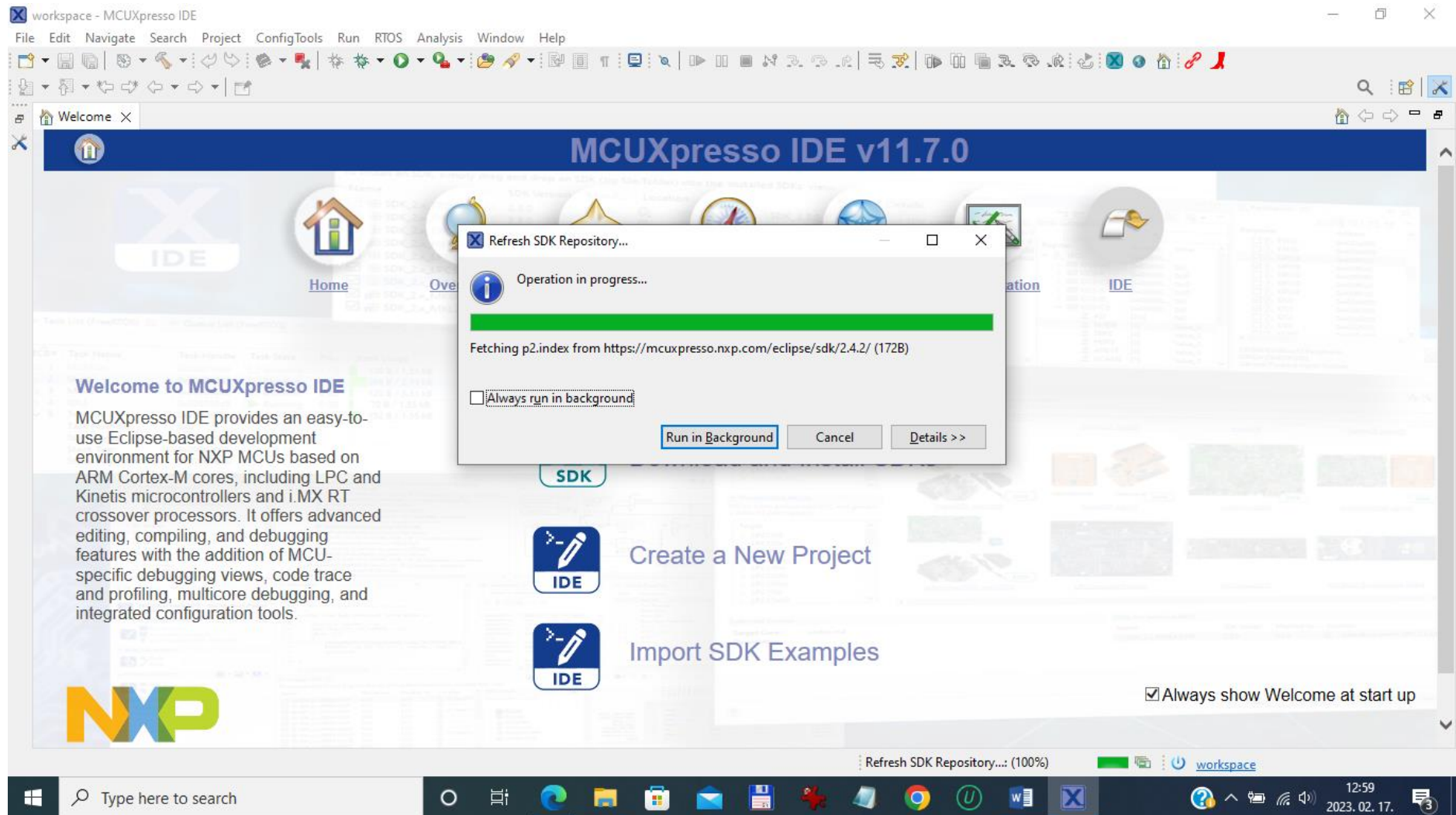
Keywords

☐ Alexa IoT Service (AIS) ☐ Alibi
 ☐ Amazon Web Service (AWS) ☐ anal
 ☐ analog-to-digital comparator ☐ anal
 ☐ Aol ☐ Asyn
 ☐ Audio ☐ Azur
 ☐ Bluetooth LE ☐ Boot
 ☐ Brown Out Detection ☐ Bus I
 ☐ CAN ☐ CAN

Keressük meg a kártyánkat: a Filtre-be írjuk be LPC845



Klikk: Install



Némi idő múlva kész. Utána Klikk Import SDK Examples

workspace - MCUXpresso IDE

File Edit Navigate Search Project ConfigTools Run RTOS Analysis Window Help

Welcome X

Home Overview What's New First Steps Web Resources Documentation IDE


Import SDK Examples

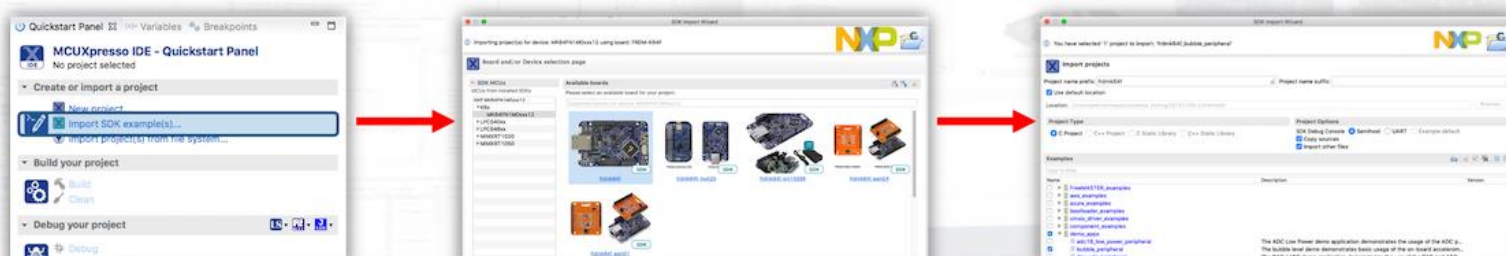
MCUXpresso IDE offers a user friendly "Import SDK Examples Wizard" that lets you easily import one or more example projects from an installed MCUXpresso SDKs into your workspace. The Wizard also allows you to make configuration changes as you import the example(s), including project naming and selecting semihosted or UART console output.

Normally the Wizard will be invoked via the "Import SDK example(s)..." option in the IDE's Quickstart Panel but you can also invoke the wizard directly from here:

☒ **Go straight to the Wizard...**

For more information, you can use the IDE User Guide link below:

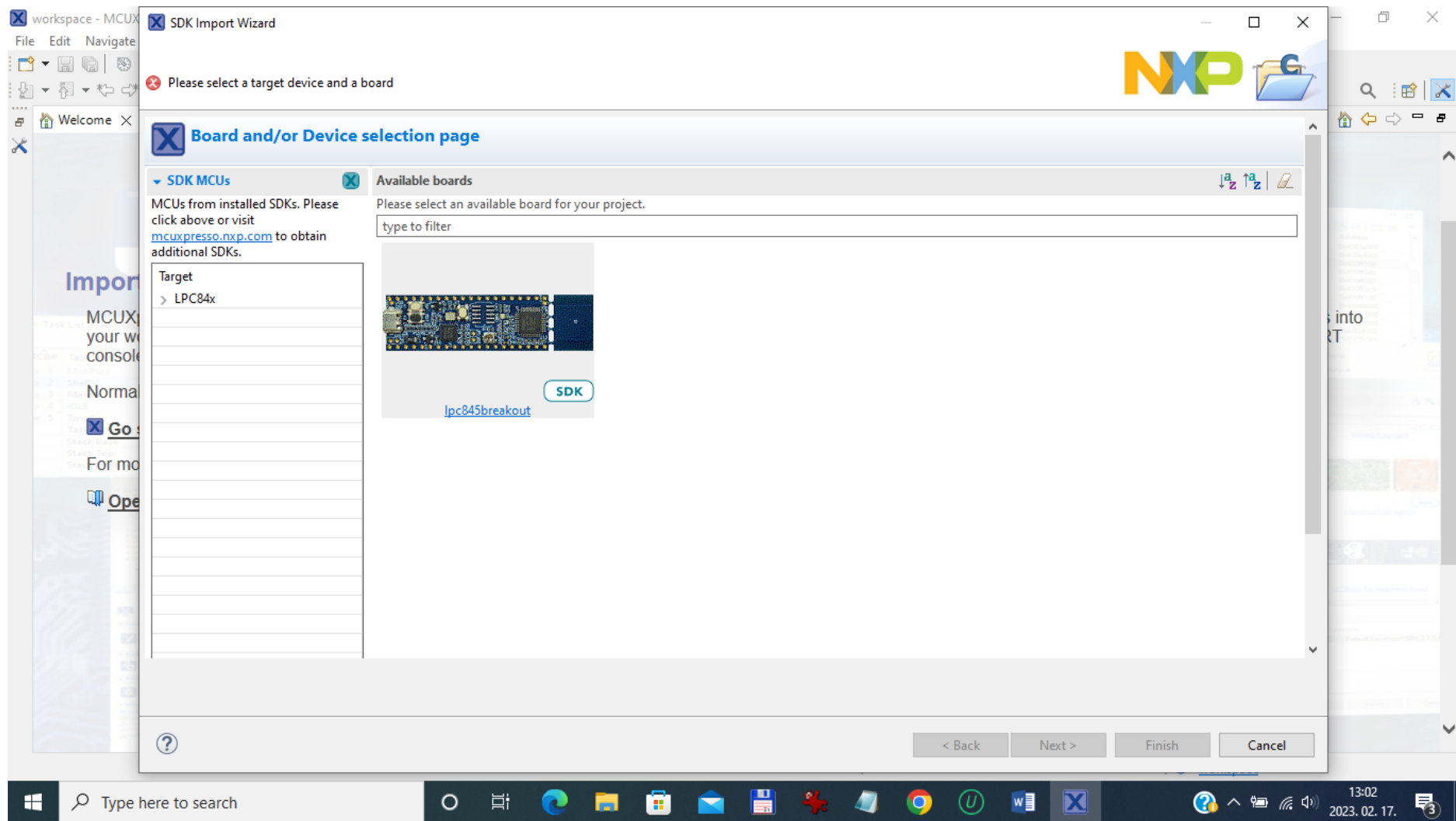
 **Open the Import SDK Examples documentation**



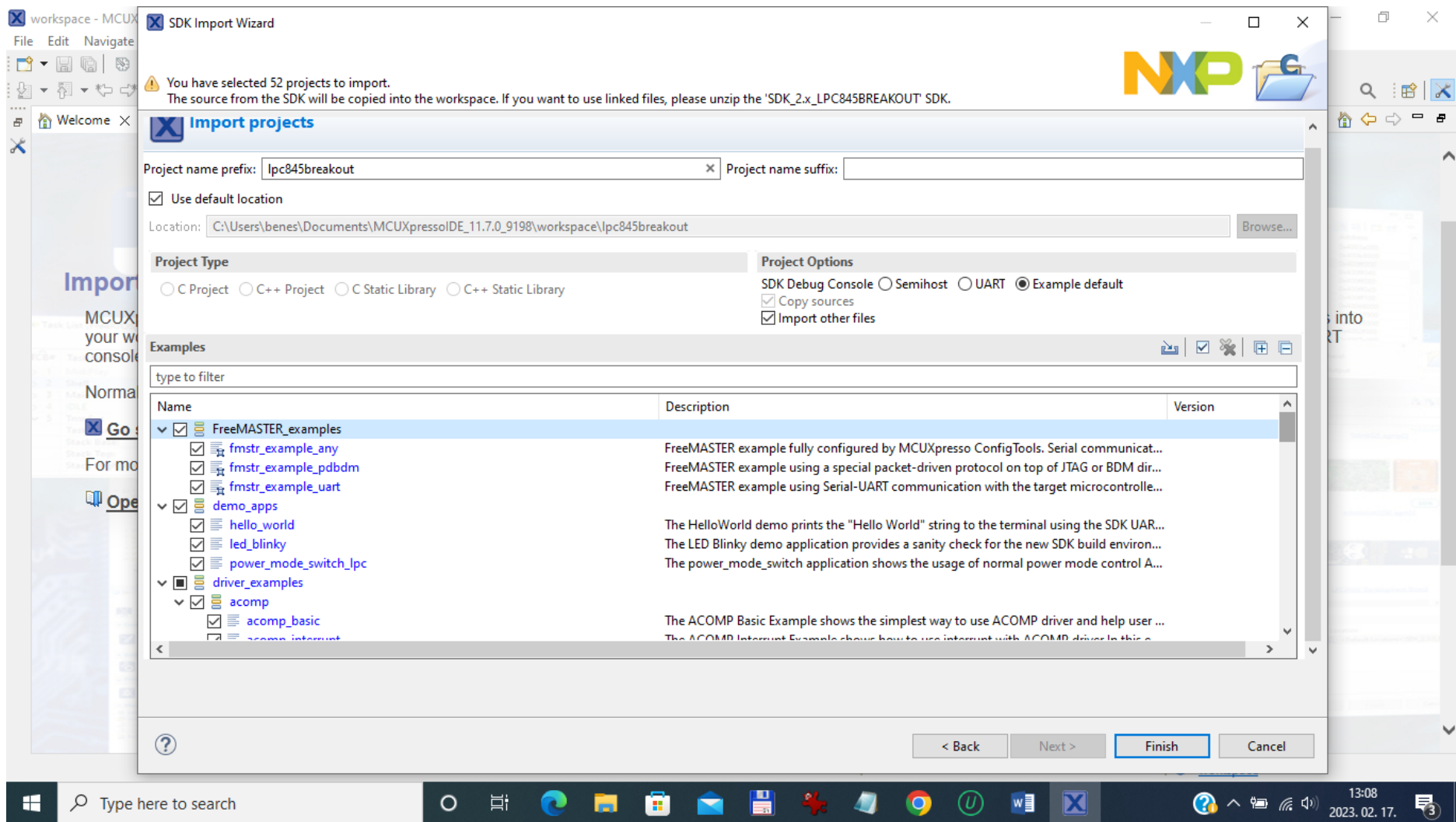
The diagram illustrates the workflow to reach the Import SDK Examples Wizard. It starts with the 'MCUXpresso IDE - Quickstart Panel' where the 'Import SDK example(s)...' option is selected. An arrow points to the 'SDK Import Wizard' window, which shows a list of available boards. Another arrow points to the 'Import projects' window, where the user can configure project options such as project name, location, and console output.

workspace

13:01
2023. 02. 17.



Klikk: SDK és NEXT



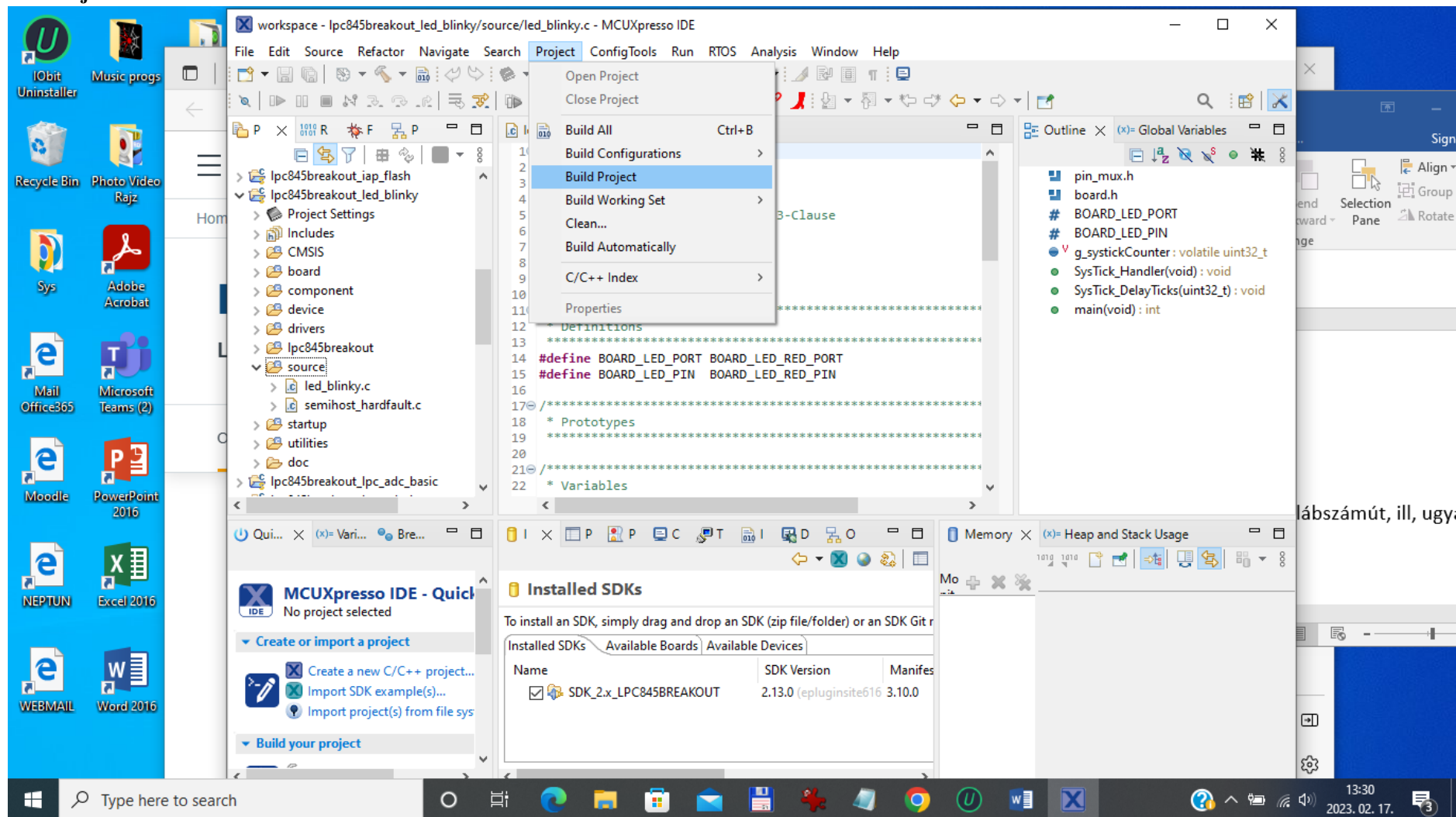
Ha van elég helyünk, akár mindet kiválaszthatjuk és Finish

Az importálás eltart pár percig...

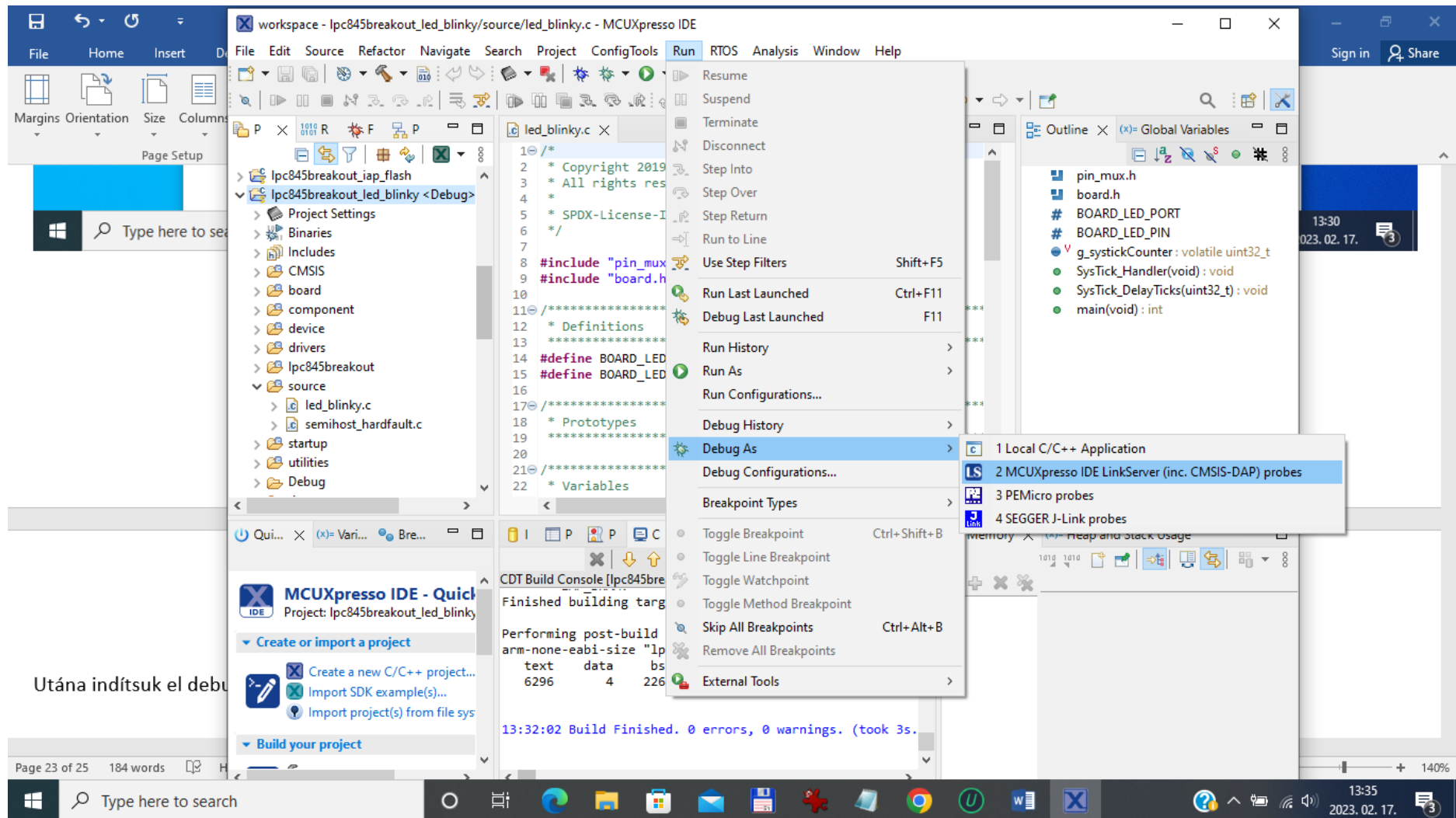
Próbáljunk ki egy projectet! Csatlakoztassuk a kártyát egyik USB portra.

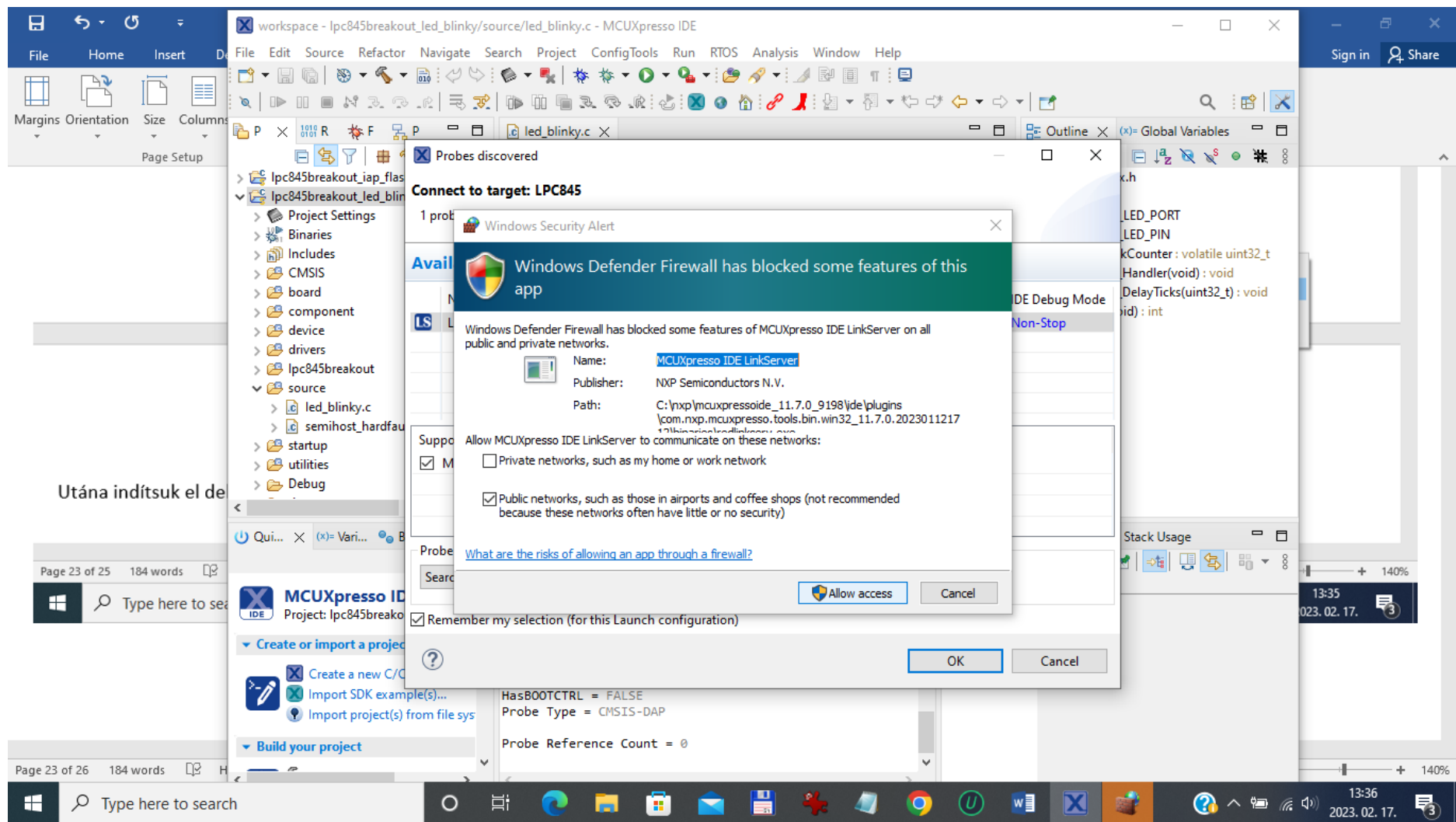
A minta projectek mindegyike ezt a processzort használja, de nem biztos, hogy ugyanezt a lábszámút, ill, ugyanezt a felesztői kártyát.

Az **lpc845breakout_led_blinky** project erre a kártyára íródott, ezt próbáljuk ki!
Klikkeljünk rá és bontsuk ki a source alkönyvtárat, ott van a minta program: **led_blinky.c**
Bild Project-el fordítsuk le!

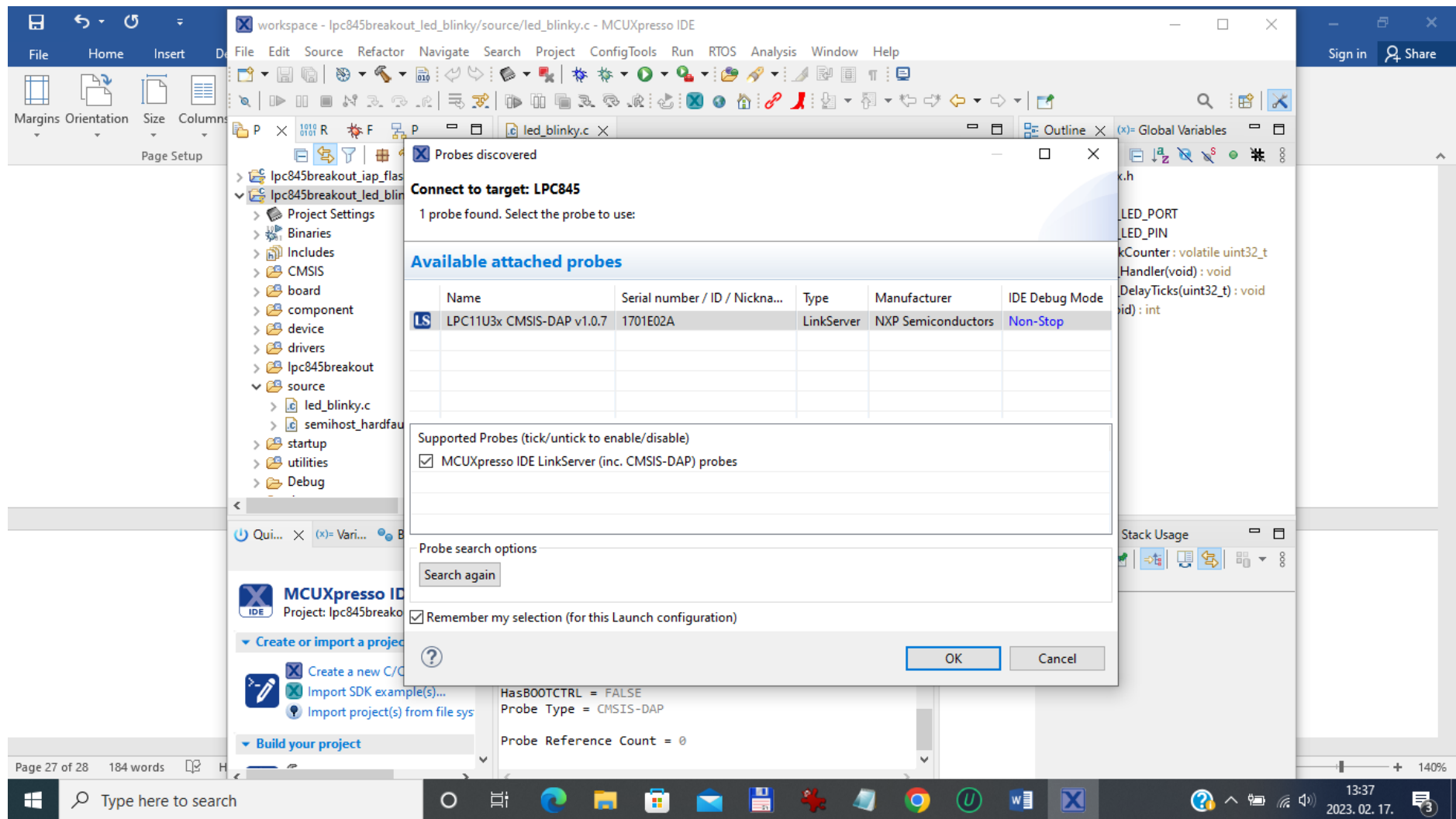


Utána indítsuk el debugger módban. Először jelöljük a ki a projec-tet a nevére klikkelve (halvány kékke válik), utána indthatjuk a Run menü alól.



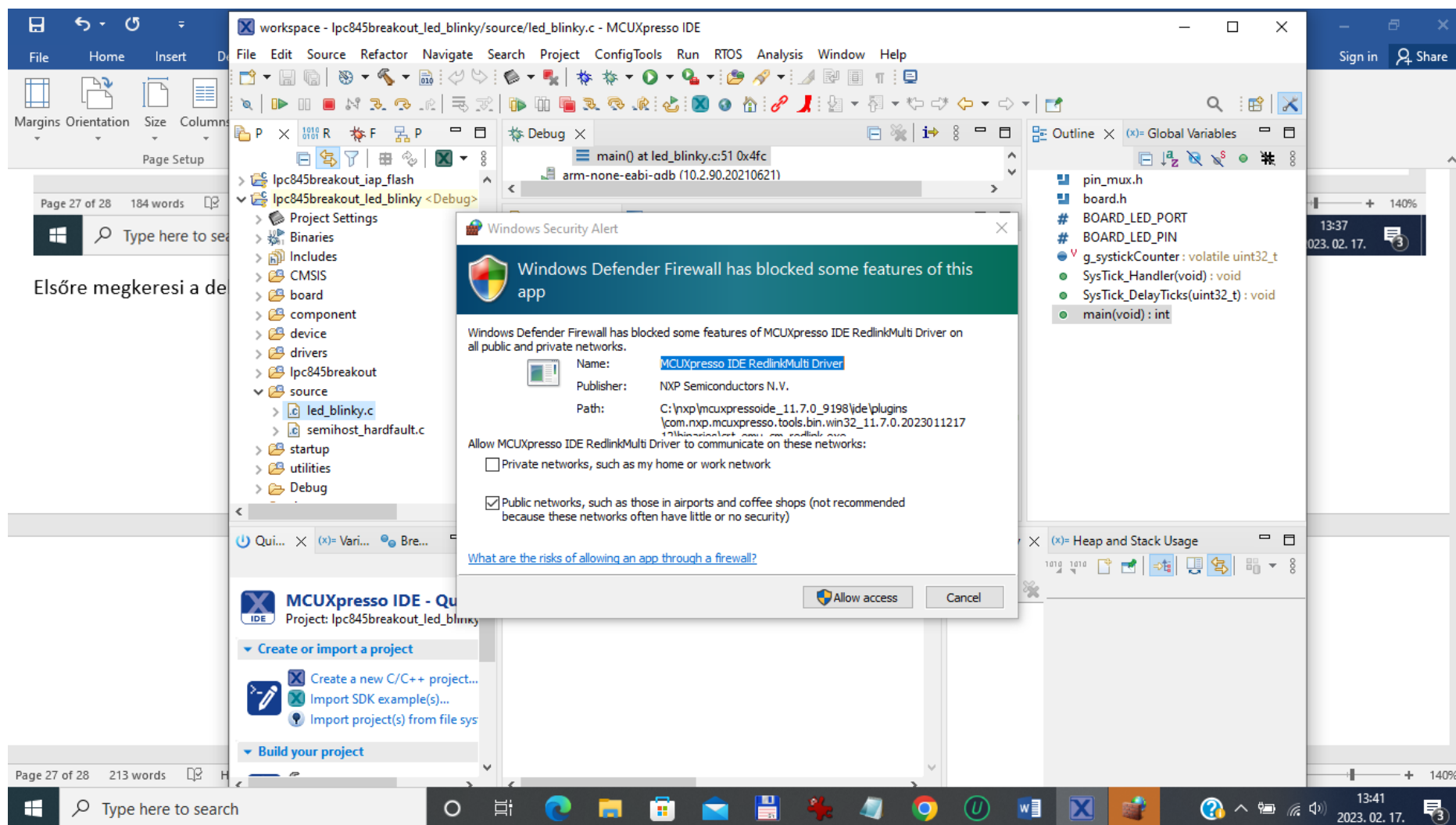


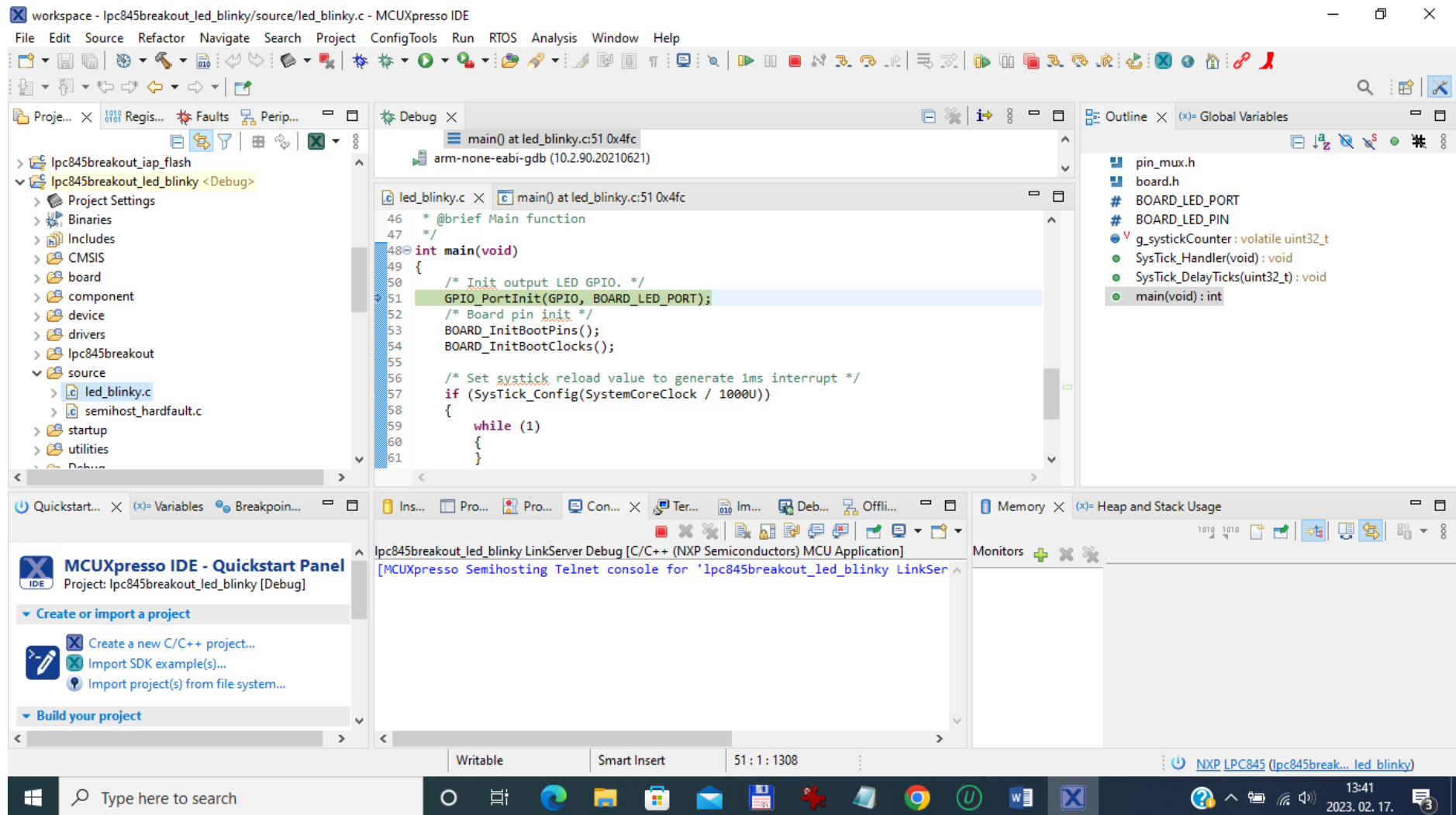
Win10 alatt egyszer bejön a Defender, ott engedjük a hozzáférést (többször nem jön be).



Elsőre megkeresi a debug hardvert, OK, utána már ismerni fogja. (Ritkán előfordul, hogy újra kéri...)

Még egyszer bejön a deffender...





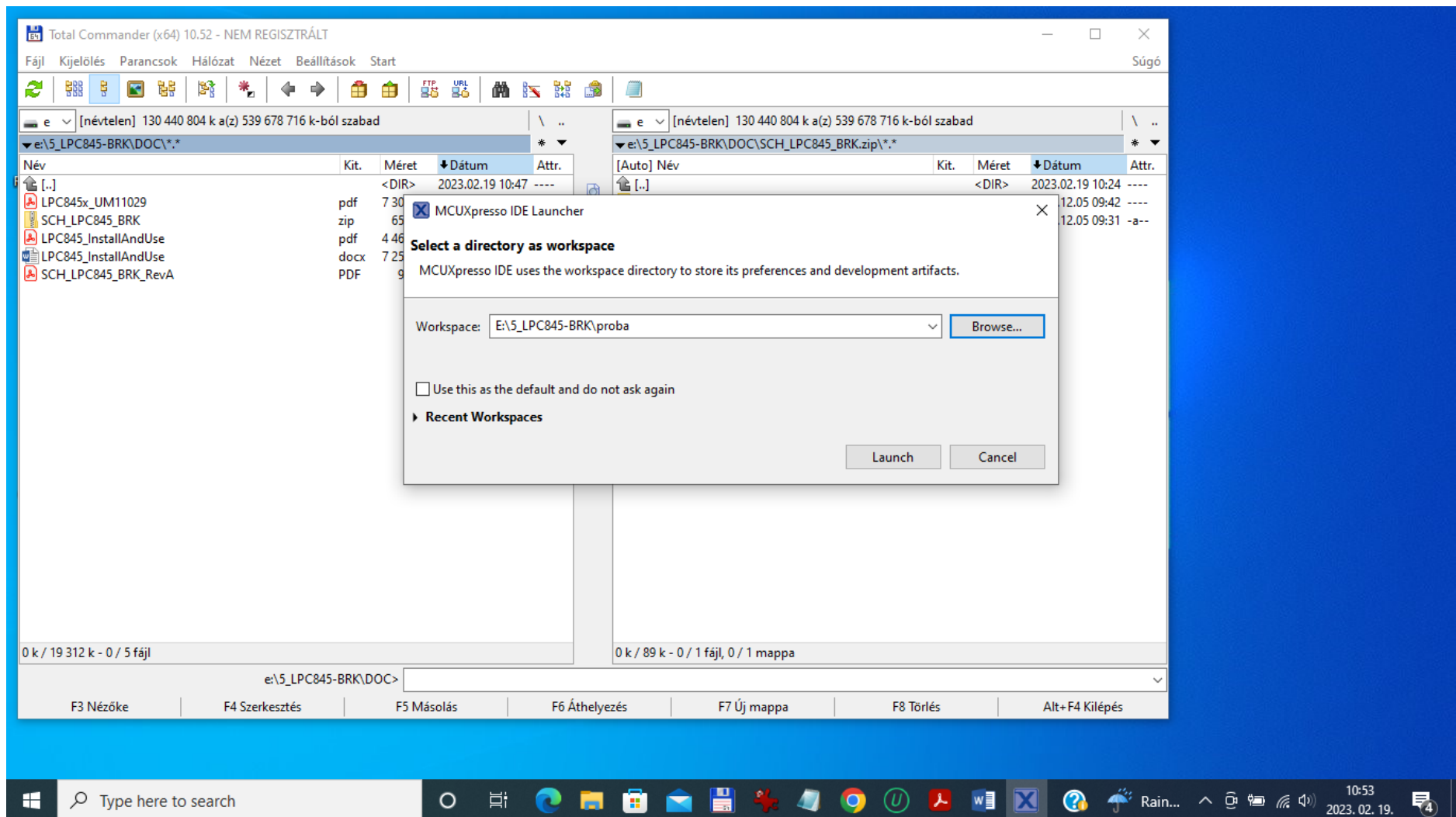
Törésponttal megáll az első sornál, ahonnan elindíthatjuk. Működnek a debugger funkciók.

Lépésenként végrehajtás: F5 bemegy a függvénybe, F6 a teljes függvényt végrehajtja.

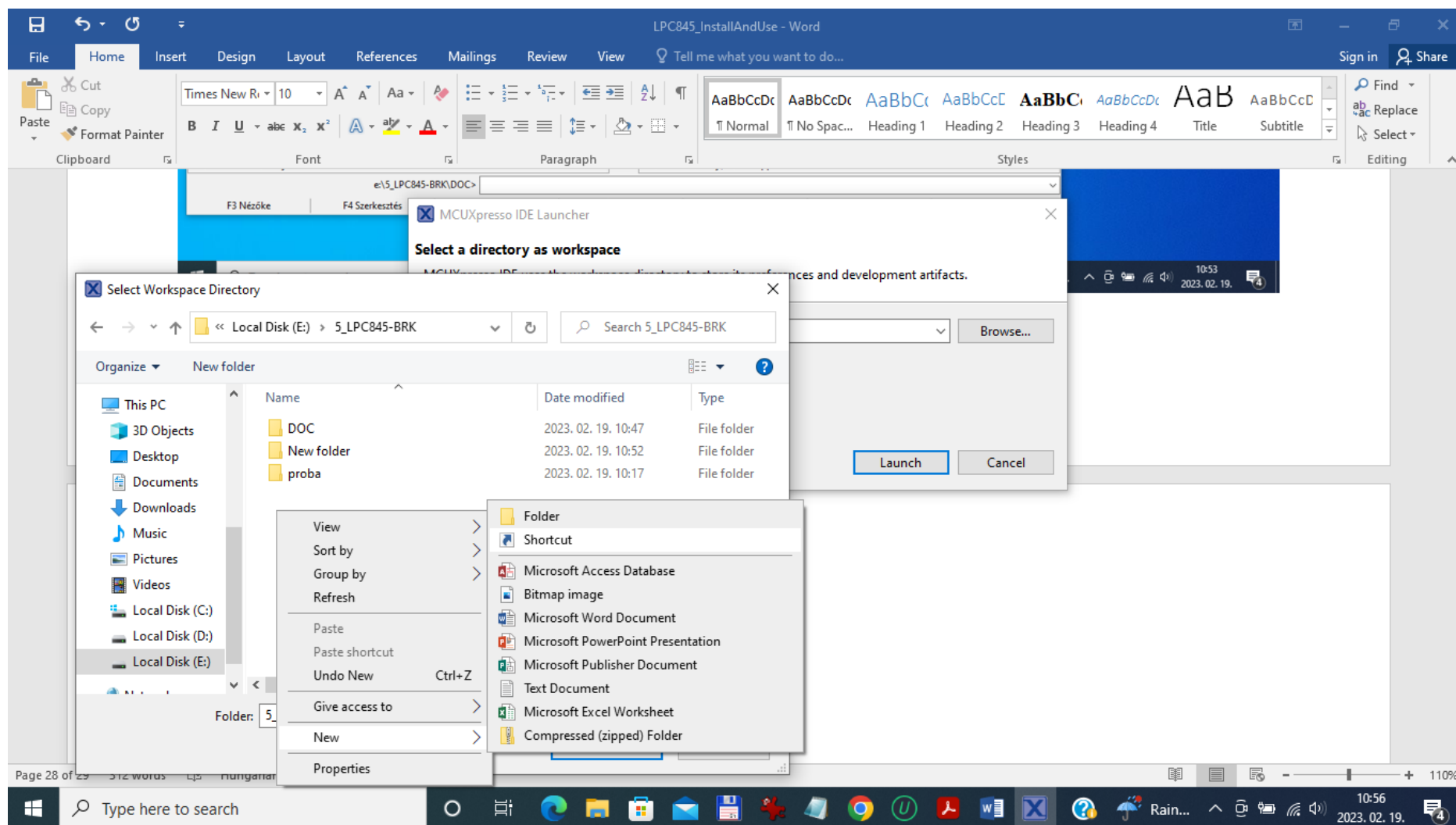
Töréspont: Kattint a sor elejére (korlátozott számú hardver töréspont lehet). Resume: a program futtatása az aktuális sortól.

Ha elindul, a vörös LED villog...

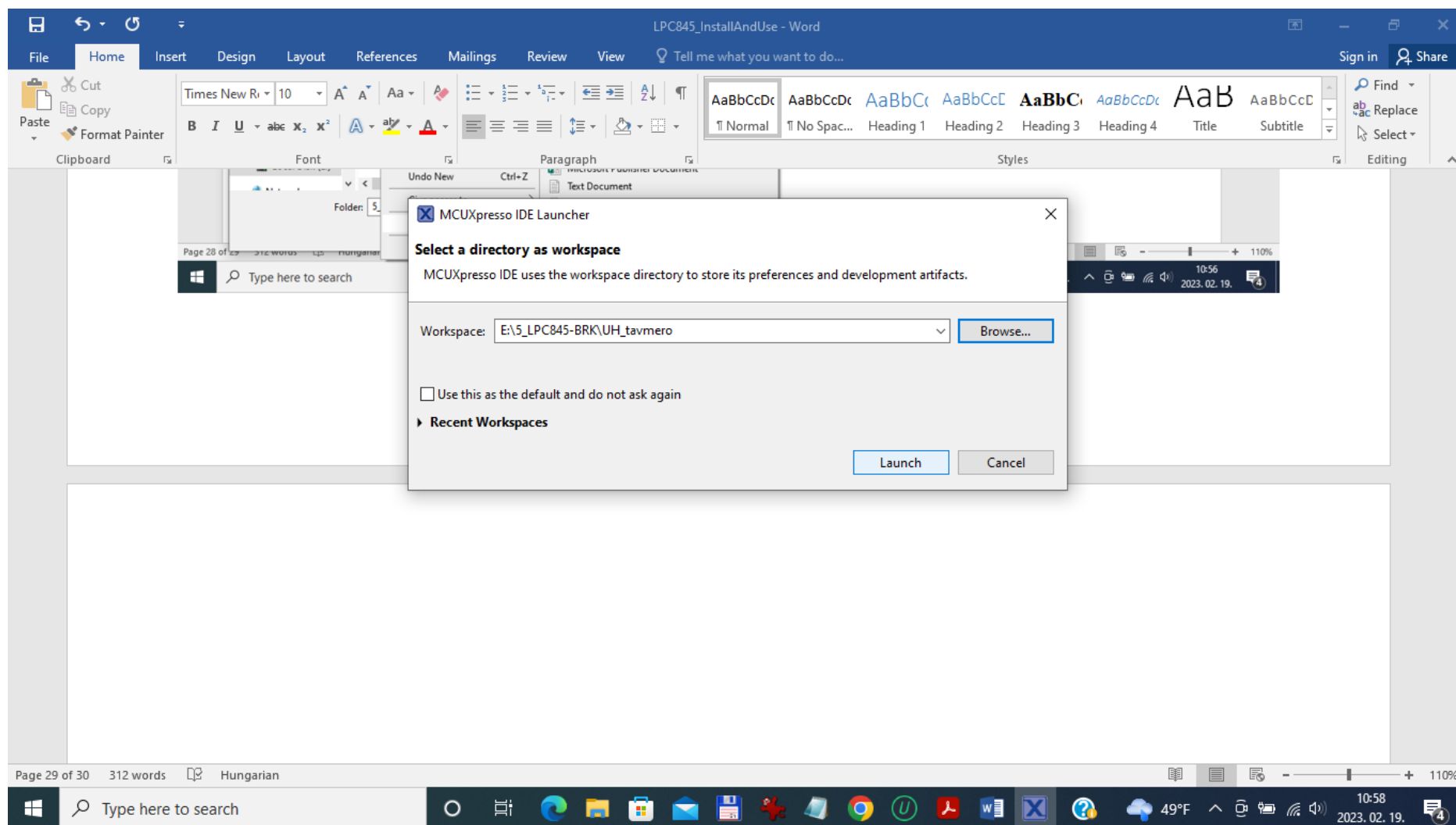
Új project létrehozása:



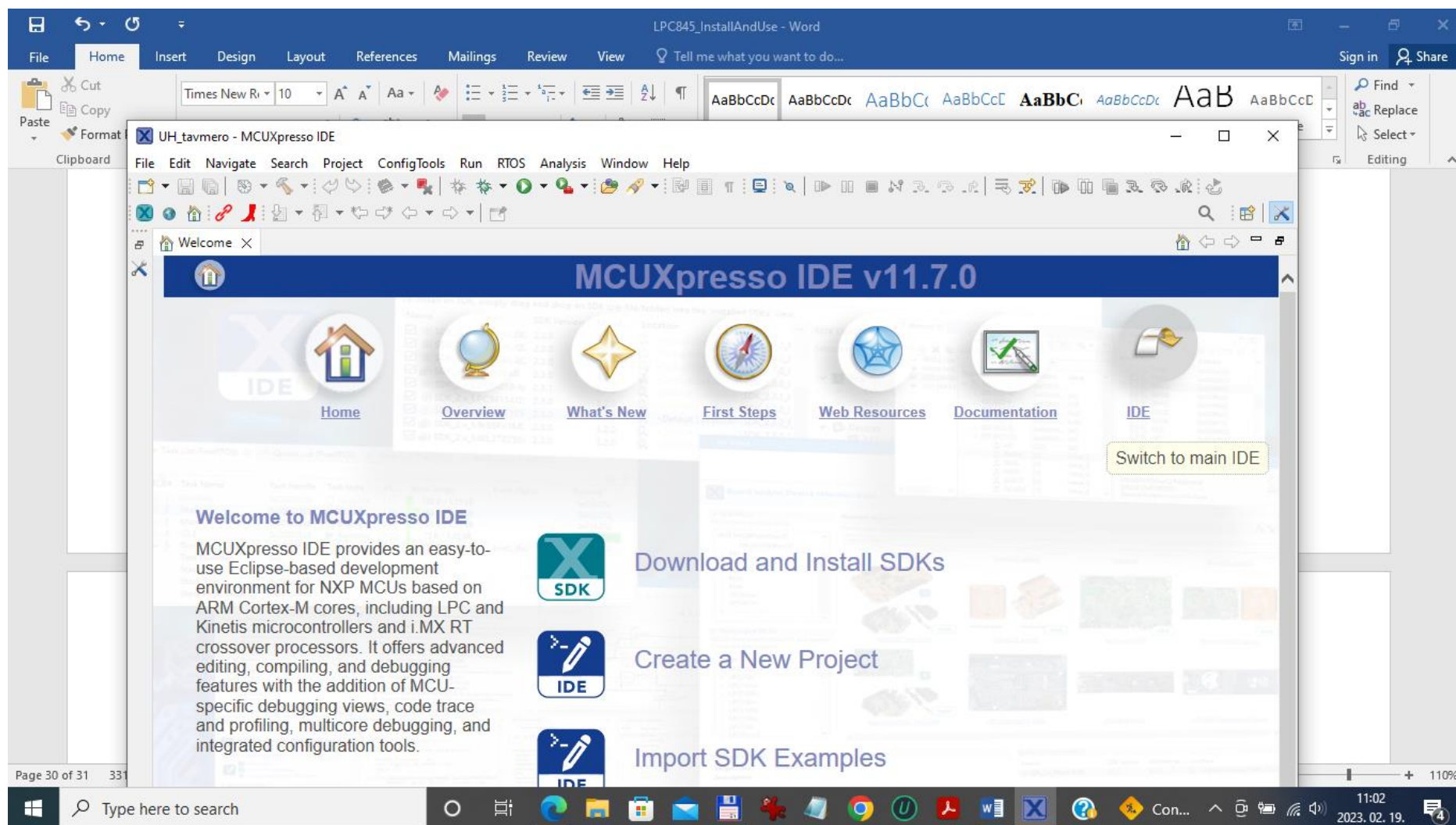
Először Browse-ra kattintás után hozzunk létre egy új workspace-t a projectünknek.



Ehhez a Browse-ra kattintás után hozzunk létre egy a projectünk nevét viselő alkönyvtárat (a mintában UH_tavmero), majd kattintsunk a Launch-re.



He bejött az MCUXpresso, kattintsunk a Create a New Project-re.

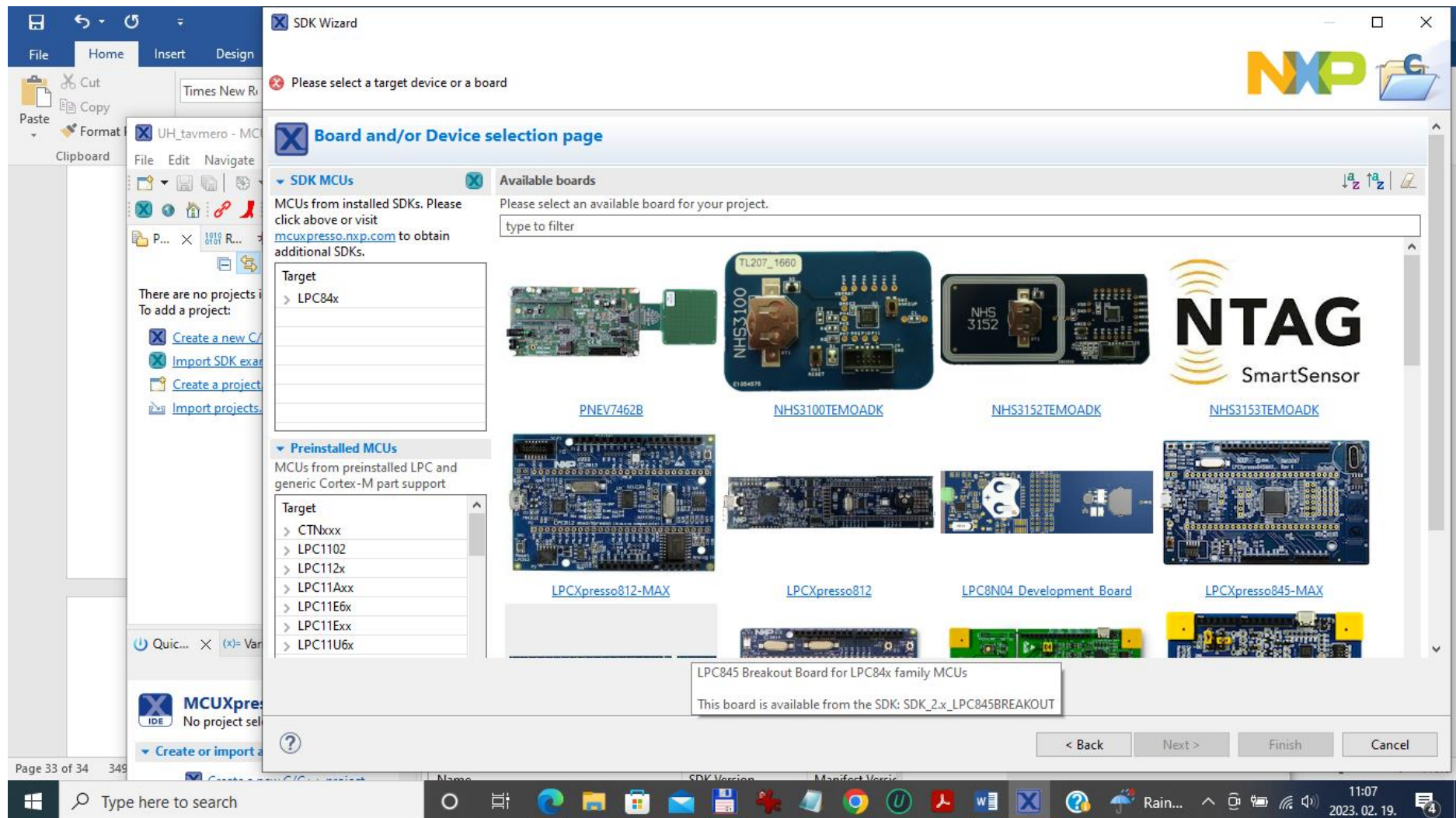


Go Straight to the Wizard

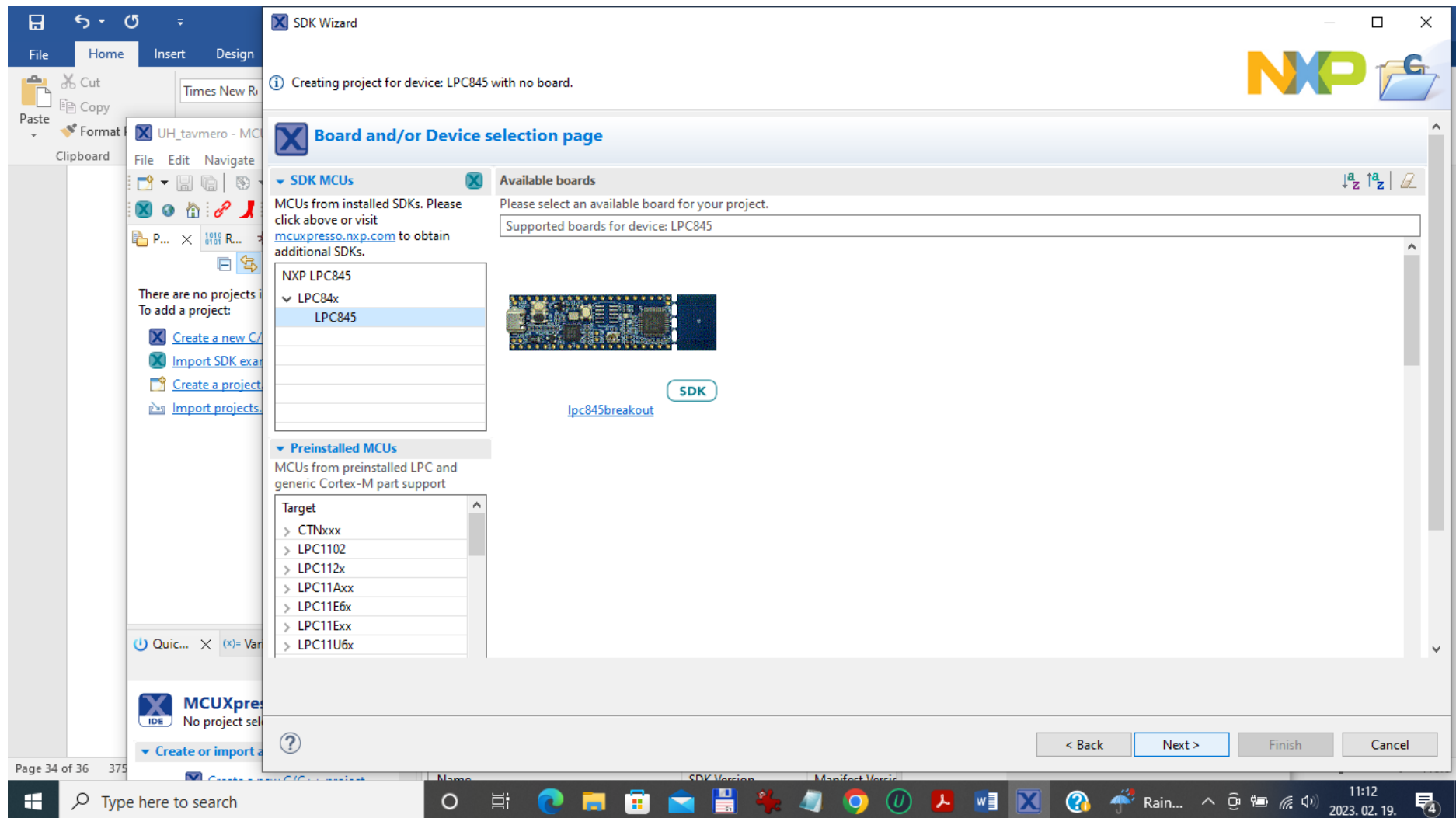
The screenshot shows the MCUXpresso IDE documentation page for "Create a New Project". The page includes a navigation bar with links like Home, Overview, What's New, First Steps, Web Resources, and Documentation. The main content area has a heading "Create a New Project" and a subheading "MCUXpresso IDE offers a user friendly 'New Project Wizard' that lets you easily create projects for your board/device." Below this, there are two paragraphs explaining the wizard's functionality and how to invoke it. A checkbox link "Go straight to the Wizard..." is highlighted. At the bottom, a workflow diagram shows the sequence of steps: "Quickstart Panel" (with "New project..." selected) → "Create project for device" dialog → "Configure the project" dialog. The Windows taskbar at the bottom shows the time as 15:42 on 2023.02.19.

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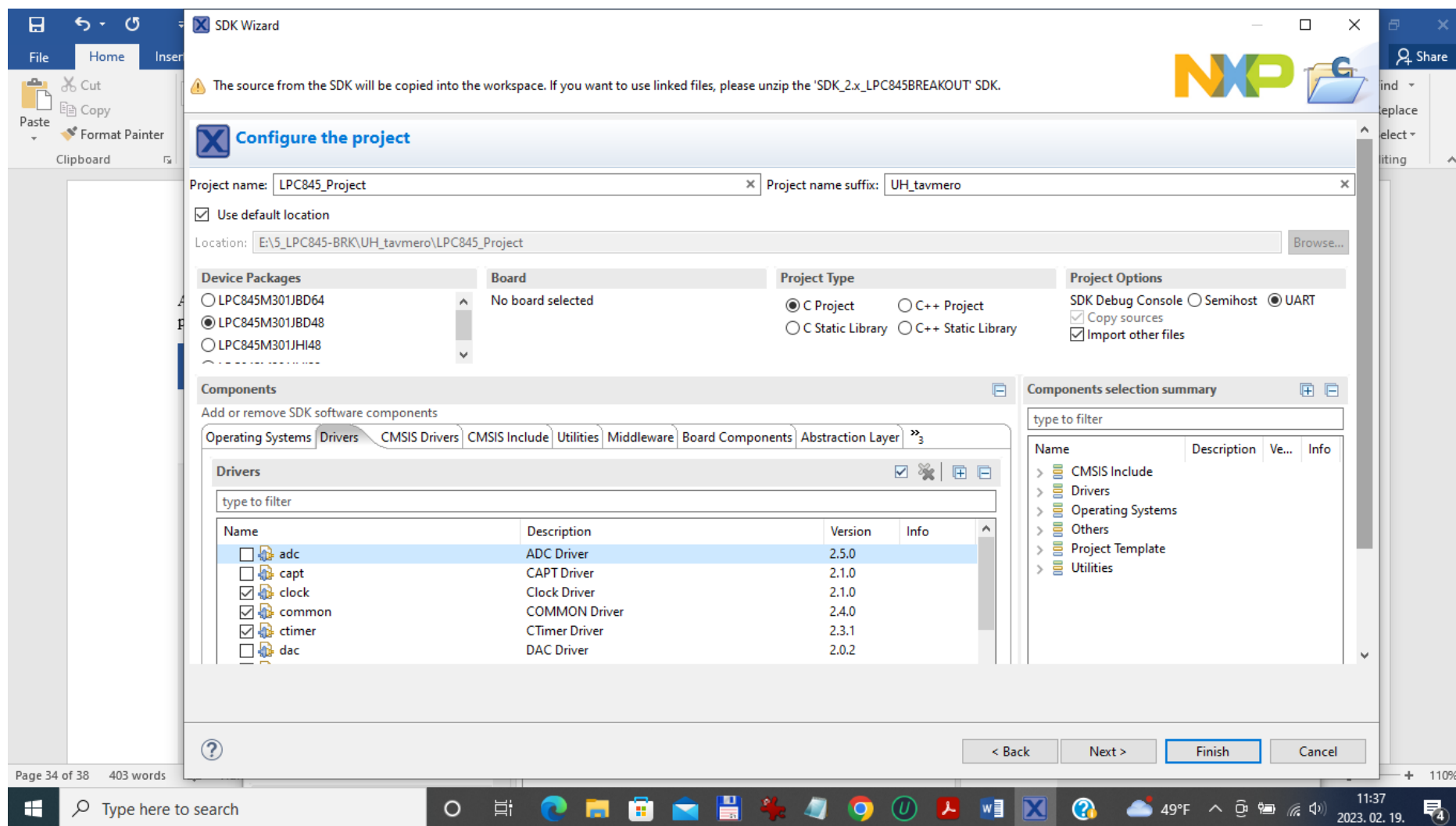
Majd válasszuk ki a az LPC84x-re kattintva az LPC845 processzort és majd előjön az egyetlen fejlesztői kártya, amely ezt tartalmazza.



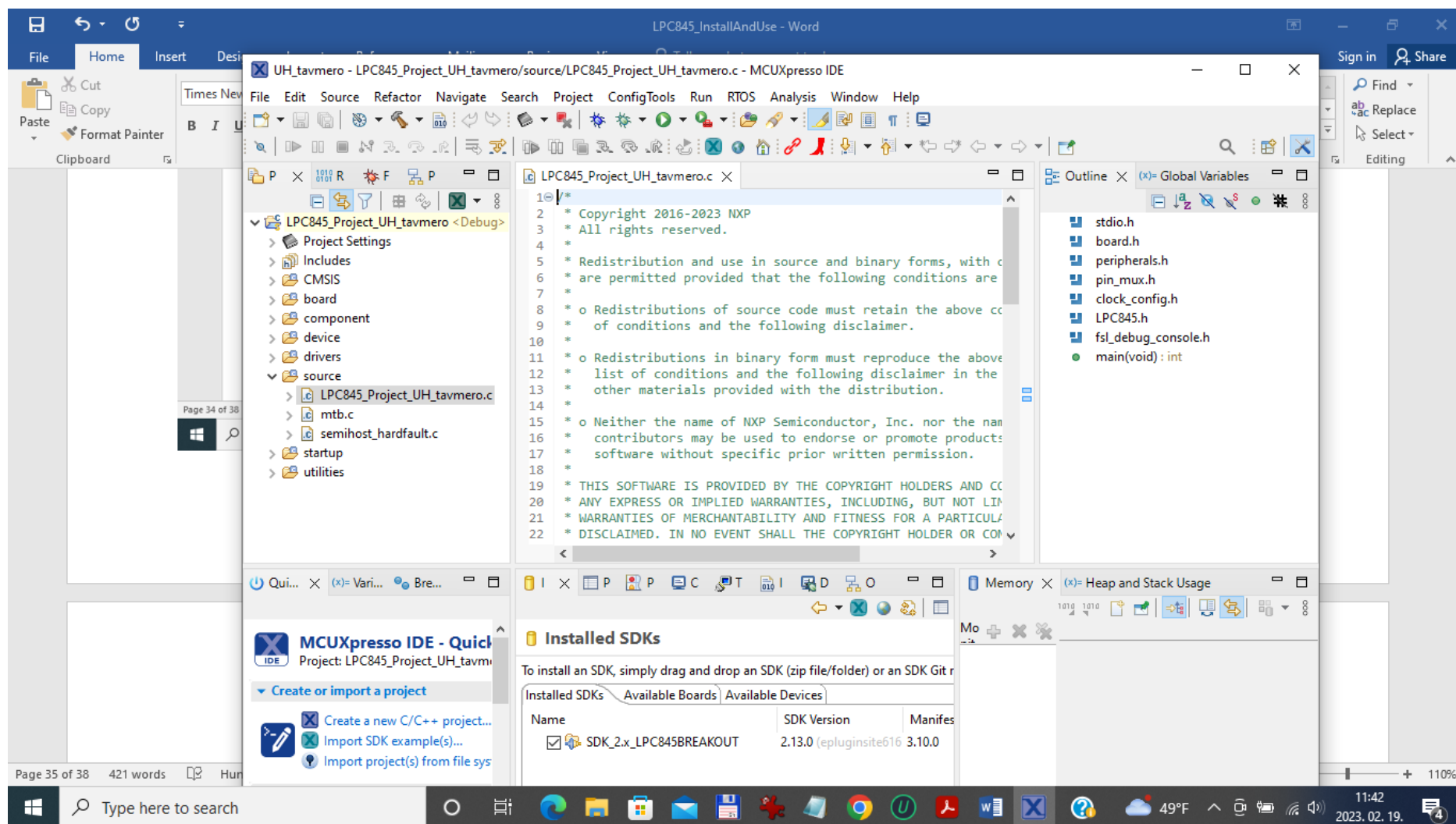
Válasszuk ki az LPC845 processzort és Next.



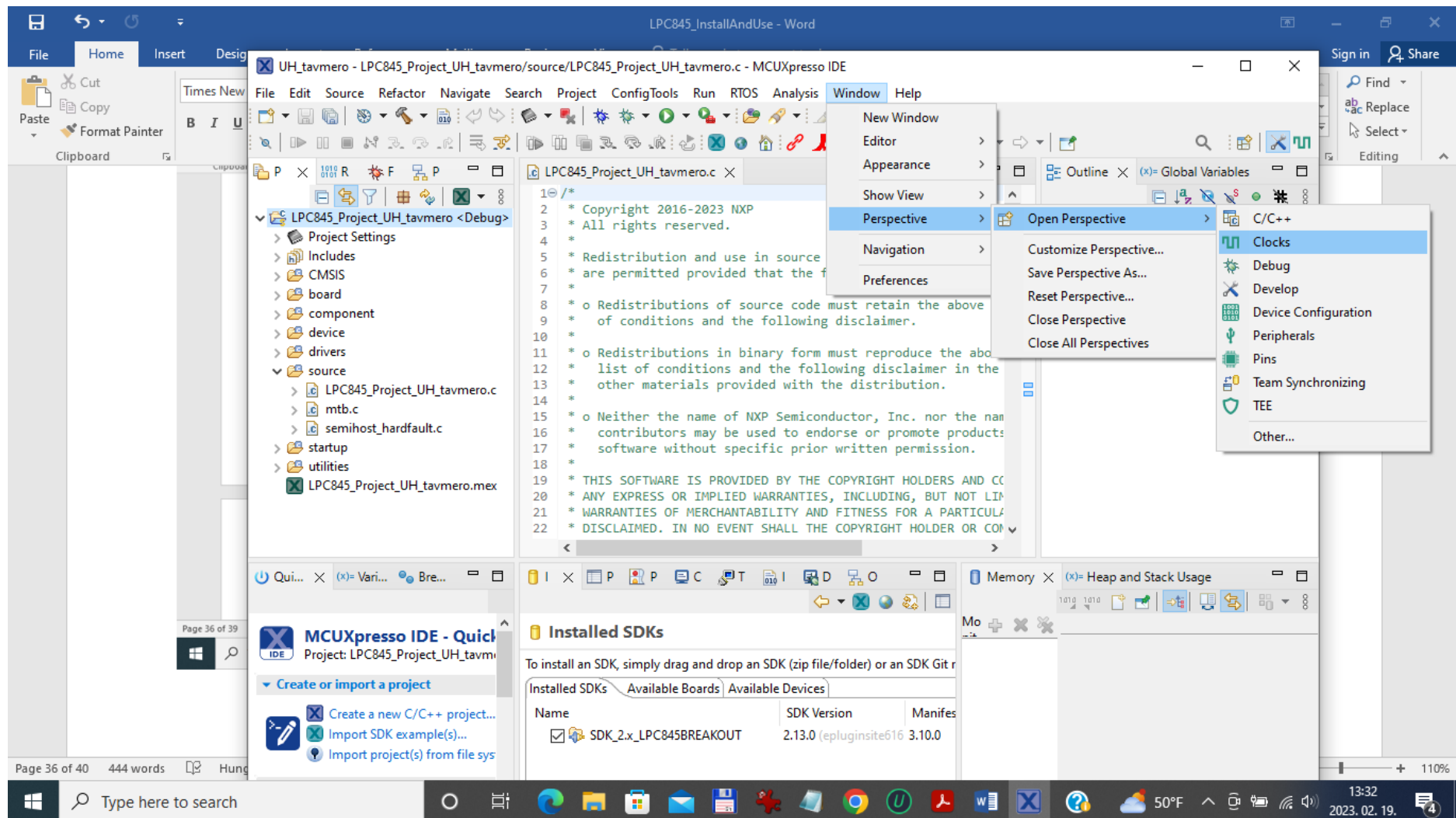
Az előző ablak jobb felső részén írjuk be a projectünk nevét (itt UH_tavmero, majd ezzel fog kiegészülni a default név, ami „LPC845project”). Bal felső részében, ha esetleg nem lenne kiválasztva, válasszuk ki az LPC845M301JBD48 processzort. Ezután a periféria listából (az előre kitöltöttekhez nem nyúlva) válasszuk ki a projectünkhöz szükséges perifériákat. A példában a ctimer-t és sctimer-t választjuk ki és Finish.



Bejön a fejlesztői (Develop) perspektíva.



Perspektíva váltáshoz jelöljük ki az aktuális projectet (katt a project nevére, ami halvány kék lesz), majd Windows/Perspective/Open Perspective/ és most a Clocks-t



Clock beállítás. Hagyhatjuk így, ha megfelel a projecthez.

UH_tavmero - LPC845_Project_UH_tavmero/board/board.h - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Clocks Run Window Help

LPC845_Project_UH_tavmero Update Code Functional Group BOARD_BootClockFRO18M

Clocks Table Clocks Diagram

Run Mode: ACTIVE

Clock Sources		Clock Outputs			
	Value	Name	Lock	Value	Accuracy
		System			
	18 MHz	System clock		18 MHz	
		Peripheral			
oscillator	Power-up	ADC clock		Inactive	
SC	Inactive	CAPT clock		Inactive	
hdog oscillator	Power-down	CLKOUT clock		Inactive	
	750 kHz	FRO clock div to 750I		750 kHz	
	10 kHz	FROHF clock		18 MHz	
ck input	<input type="checkbox"/> Inactive	I2C0 clock		Inactive	
oscillator	Power-up	I2C1 clock		Inactive	
mines ...uency range.	1 - 20 MHz frequency range.	I2C2 clock		Inactive	
ss system oscillator	Active	I2C3 clock		Inactive	
clock input	<input type="checkbox"/> Inactive	IOCON0 clock		Inactive	
		IOCON1 clock		Inactive	
		IOCON2 clock		Inactive	
		IOCON3 clock		Inactive	
		IOCON4 clock		Inactive	
		IOCON5 clock		Inactive	
		IOCON6 clock		Inactive	
		LowPower oscillator		10 kHz	
		SCT clock		Inactive	
		SPI0 clock		Inactive	
		SPI1 clock		Inactive	
		SYSPLL clock		Inactive	
		UART0 clock		Inactive	
		UART1 clock		Inactive	

Configuration - General Info

Configuration - HW Info

Processor: LPC845

Part number: LPC845M301JBD48

Core: Cortex-M0P

Board: LPC845BREAKOUT

SDK Version: ksdk2_0

Project

Clocks

Configures the initialization of the core, system, bus, and peripheral clocks.

Generated code

☒ Update code enabled

Problems

type filter text

Level	Resource	Issue
-------	----------	-------

LPC845_Project_UH_tavmero

NXP LPC845* (LPC845 Pr...H tavmero)

17:37 2023.02.19.

A fejlesztői kártya órajele 18 MHz, 24 MHz vagy 30 MHz lehet. Itt 30 MHz-et állítottunk be.

UH_tavmero - LPC845_Project_UH_tavmero/board/board.h - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Clocks Run Window Help

LPC845_Project_UH_tavmero Update Code Functional Group BOARD_BootClockFRO18M

Clocks Table Clocks Diagram

Run Mode: ACTIVE

Clock Sources		Clock Outputs	
Name	Value	Name	Value
Internal		Peripheral	
FRO_OSC	30 MHz	ADC clock	Inactive
FRO oscillator	30 MHz	CAPT clock	Inactive
WWDT OSC	24 MHz	CTB clock	Inactive
Watchdog oscillator	18 MHz	FRO clock div to 750I	750 kHz
divto750k	750 kHz	FROHF clock	30 MHz
lpsc	10 kHz	I2C0 clock	Inactive
External		I2C1 clock	Inactive
Main clock input	Inactive	I2C2 clock	Inactive
main oscillator	Power-up	I2C3 clock	Inactive
Determines frequency range.	1 - 20 MHz frequency range.	IOCON0 clock	Inactive
Bypass system oscillator	Active	IOCON1 clock	Inactive
External clock input	Inactive	IOCON2 clock	Inactive
		IOCON3 clock	Inactive
		IOCON4 clock	Inactive
		IOCON5 clock	Inactive
		IOCON6 clock	Inactive
		LowPower oscillator	10 kHz
		SCT clock	Inactive
		SPI0 clock	Inactive
		SPI1 clock	Inactive
		SYSPLL clock	Inactive
		UART0 clock	Inactive
		UART1 clock	Inactive
		UART2 clock	Inactive

A valid value must be a frequency. It should contain a number and a unit. E.g. 4 MHz.

Element Details: SYSCON.fro_osc

Name	C...	L...	Value	A...
FRO_OSC			30 MHz	
FRO oscillator			Power-up	

Problems

type filter text

Level	Resource	Issue	Origin
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LPC845_Project_UH_tavmero

NXP LPC845* (LPC845 Pr...H tavmero)

17:43 2023.02.19.

Így a rendszer órajele (és az AHBCLK órajele, ami az ARM AMBA High-performance Busz órajele) is 30 MHz lesz.

UH_tavmero - LPC845_Project_UH_tavmero/board/board.h - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Clocks Run Window Help

LPC845_Project_UH_tavmero Update Code Functional Group BOARD_BootClockFRO18M

Clocks Table Clocks Diagram

Run Mode: ACTIVE

Clock Sources		Clock Outputs	
Name	Value	Name	Value
Internal		System	
FRO_OSC	30 MHz	System clock	30 MHz
FRO oscillator	Power-up	Peripheral	
WWDT OSC	Inactive	ADC clock	Inactive
Watchdog oscillator	Power-down	CAPT clock	Inactive
divto750k	750 kHz	CLKOUT clock	Inactive
lpsc	10 kHz	FRO clock div to 750l	750 kHz
External		FROHF clock	30 MHz
Main clock input	<input type="checkbox"/> Inactive	I2C0 clock	Inactive
main oscillator	Power-up	I2C1 clock	Inactive
Determines ...uency range.	1 - 20 MHz frequency range.	I2C2 clock	Inactive
Bypass system oscillator	<input checked="" type="checkbox"/> Active	I2C3 clock	Inactive
External clock input	<input type="checkbox"/> Inactive	IOCON0 clock	Inactive
		IOCON1 clock	Inactive
		IOCON2 clock	Inactive
		IOCON3 clock	Inactive
		IOCON4 clock	Inactive
		IOCON5 clock	Inactive
		IOCON6 clock	Inactive
		LowPower oscillator	10 kHz
		SCT clock	1 MHz
		SPI0 clock	Inactive
		SPI1 clock	Inactive
		SYSPLL clock	Inactive
		UART0 clock	30 MHz

Path Details: System_clock

Name	C...	L...	Value	A...
FRO_OSC			30 MHz	
FRO oscillator			Power-up	
fro			fro clock is direct from FRO oscillator	
Main clock select			FRO	
Main clock PLL select			main_clk_pre_pll	
SYSAHBCLKDIV			/ 1	
SYSAHBCLKDIV Frequency			30 MHz	
AHBCLK Frequency			30 MHz	
System clock			30 MHz	

Problems

type filter text

Level	Resource	Issue	Origin

LPC845_Project_UH_tavmero

NXP LPC845* (LPC845 Pr...H tavmero)

18:06 2023. 02. 19.

A perifériák órajelének beállításához kattunk a perifériára (itt **SCTIMER**).

UH_tavmero - LPC845_Project_UH_tavmero/board/board.h - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Clocks Run Window Help

LPC845_Project_UH_tavmero Update Code Functional Group BOARD_BootClockFRO18M

Clocks Table Clocks Diagram

Run Mode: ACTIVE

Clock Sources		Clock Outputs	
Name	Value	Name	Value
Internal		Peripheral	
FRO_OSC	30 MHz	ADC clock	Inactive
FRO oscillator	Power-up	CAPT clock	Inactive
WWDTC OSC	Inactive	CLKOUT clock	Inactive
Watchdog oscillator	Power-down	FRO clock div to 750I	750 kHz
divto750k	750 kHz	FROHF clock	30 MHz
lpsc	10 kHz	I2C0 clock	Inactive
External		I2C1 clock	Inactive
Main clock input	<input type="checkbox"/> Inactive	I2C2 clock	Inactive
main oscillator	Power-up	I2C3 clock	Inactive
Determines ...uency range.	1 - 20 MHz frequency range.	IOCON0 clock	Inactive
Bypass system oscillator	Active	IOCON1 clock	Inactive
External clock input	<input type="checkbox"/> Inactive	IOCON2 clock	Inactive
		IOCON3 clock	Inactive
		IOCON4 clock	Inactive
		IOCON5 clock	Inactive
		IOCON6 clock	Inactive
		LowPower oscillator	10 kHz
		SCT clock	Inactive
		SPI0 clock	Inactive
		SPI1 clock	Inactive
		SYSPLL clock	Inactive
		UART0 clock	Inactive
		UART1 clock	Inactive
		UART2 clock	Inactive

Path Details: SCT_clock

Name	C...	L...	Value	A...
SCT clock select			none	
SCTCLKDIV			Disabled	
SCTCLKDIV Frequency			Inactive	
SCTCLK Frequency			Inactive	
SCT clock			Inactive	

Problems

type filter text

Level	Resource	Issue	Origin
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LPC845_Project_UH_tavmero

NXP LPC845* (LPC845 Pr...H tavmero)

17:47 2023.02.19.

A jobb oldalt megnyíló ablakban az órajel forrást választjuk ki (most SCT clock select). Válasszuk az FRO-t.

UH_tavmero - LPC845_Project_UH_tavmero/board/board.h - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Clocks Run Window Help

LPC845_Project_UH_tavmero Update Code Functional Group BOARD_BootClockFRO18M

Clocks Table Clocks Diagram

Run Mode: ACTIVE

Clock Sources

Name	C...	Value
Internal		
FRO_OSC		30 MHz
FRO oscillator		Power-up
WWDOT OSC		Inactive
Watchdog oscillator		Power-down
divto750k		750 kHz
lpsc		10 kHz
External		
Main clock input	<input type="checkbox"/>	Inactive
main oscillator		Power-up
Determines ...uency range.		1 - 20 MHz frequency range.
Bypass system oscillator		Active
External clock input	<input type="checkbox"/>	Inactive

Clock Outputs

Name	Lock	Value
Peripheral		
ADC clock		Inactive
CAPT clock		Inactive
CLKOUT clock		Inactive
FRO clock div to 750I		750 kHz
FROHF clock		30 MHz
I2C0 clock		Inactive
I2C1 clock		Inactive
I2C2 clock		Inactive
I2C3 clock		Inactive
IOCON0 clock		Inactive
IOCON1 clock		Inactive
IOCON2 clock		Inactive
IOCON3 clock		Inactive
IOCON4 clock		Inactive
IOCON5 clock		Inactive
IOCON6 clock		Inactive
LowPower oscillator		10 kHz
SCT clock		Inactive
SPI0 clock		Inactive
SPI1 clock		Inactive
SYSPLL clock		Inactive
UART0 clock		Inactive
UART1 clock		Inactive
UART2 clock		Inactive

Path Details: SCT_clock

Name	C...	L...	Value	A...
SCT clock select			none	
SCTCLKDIV			FRO	
SCTCLKDIV Frequency			main clock	
SCTCLK Frequency			none	
SCT clock			sys pll	

Problems

type filter text

Level	Resource	Issue	Origin
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LPC845_Project_UH_tavmero

NXP LPC845* (LPC845 Pr...H tavmero)

17:50 2023.02.19.

Válasszuk az órajel előosztását (STCKLDIV) 30-nak, így 1 MHz-el (1u sec-onként) fog lépni.

UH_tavmero - LPC845_Project_UH_tavmero/board/board.h - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Clocks Run Window Help

LPC845_Project_UH_tavmero Update Code Functional Group BOARD_BootClockFRO18M

Clocks Table Clocks Diagram

Run Mode ACTIVE

Clock Sources	
Name	Value
Internal	
FRO_OSC	30 MHz
FRO oscillator	Power-up
WWDTC OSC	Inactive
Watchdog oscillator	Power-down
divto750k	750 kHz
lpsc	10 kHz
External	
Main clock input	<input type="checkbox"/> Inactive
main oscillator	Power-up
Determines frequency range.	1 - 20 MHz frequency range.
Bypass system oscillator	Active
External clock input	<input type="checkbox"/> Inactive

Clock Outputs		
Name	Lock	Value
Peripheral		
ADC clock	Inactive	
CAPT clock	Inactive	
CLKOUT clock	Inactive	
FRO clock div to 750I		750 kHz
FROHF clock		30 MHz
I2C0 clock	Inactive	
I2C1 clock	Inactive	
I2C2 clock	Inactive	
I2C3 clock	Inactive	
IOCON0 clock	Inactive	
IOCON1 clock	Inactive	
IOCON2 clock	Inactive	
IOCON3 clock	Inactive	
IOCON4 clock	Inactive	
IOCON5 clock	Inactive	
IOCON6 clock	Inactive	
LowPower oscillator		10 kHz
SCT clock		30 MHz
SPI0 clock	Inactive	
SPI1 clock	Inactive	
SYSPLL clock	Inactive	
UART0 clock	Inactive	
UART1 clock	Inactive	
UART2 clock	Inactive	

Path Details: SCT_clock

Name	C...	L...	Value	A...
FRO_OSC			30 MHz	
FRO oscillator			Power-up	
fro			fro clock is direct from FRO oscillator	
SCT clock select			FRO	
SCTCLKDIV			/ 1	
SCTCLKDIV Frequency			30 MHz	
SCTCLK Frequency			30 MHz	
SCT clock			30 MHz	

Problems

type filter text

Level	Resource	Issue	Origin
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LPC845_Project_UH_tavmero

NXP LPC845* (LPC845 Pr...H tavmero)

17:52 2023.02.19.

Az UART0 órajelét is (UART clock select) válasszuk FRO-nak.

Az Update code-ra klikkelve a program megfelelő részeit a beállításoknak megfelelően módosítja és visszatér Develop perspektívába.

UH_tavmero - LPC845_Project_UH_tavmero/board/board.h - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Clocks Run Window Help

LPC845_Project_UH_tavmero Update Code Functional Group BOARD_BootClockFRO18M

Clocks Table Clocks Diagram

Run Mode ACTIVE

Clock Sources		Clock Outputs	
Name	Value	Name	Value
Internal		CLKOUT clock	Inactive
FRO_OSC	30 MHz	FRO clock div to 750k	750 kHz
FRO oscillator	Power-up	FROHF clock	30 MHz
WWDT OSC	Inactive	I2C0 clock	Inactive
Watchdog oscillator	Power-down	I2C1 clock	Inactive
divto750k	750 kHz	I2C2 clock	Inactive
lpsc	10 kHz	I2C3 clock	Inactive
External		IOCON0 clock	Inactive
Main clock input	<input type="checkbox"/> Inactive	IOCON1 clock	Inactive
main oscillator	Power-up	IOCON2 clock	Inactive
Determines ...uency range.	1 - 20 MHz frequency range.	IOCON3 clock	Inactive
Bypass system oscillator	Active	IOCON4 clock	Inactive
External clock input	<input type="checkbox"/> Inactive	IOCON5 clock	Inactive
		IOCON6 clock	Inactive
		LowPower oscillator	10 kHz
		SCT clock	1 MHz
		SPI0 clock	Inactive
		SPI1 clock	Inactive
		SYSPLL clock	Inactive
		UART0 clock	30 MHz
		UART1 clock	Inactive
		UART2 clock	Inactive
		UART3 clock	Inactive
		UART4 clock	Inactive
		WWDT clock	Inactive

Path Details: UART0_clock

Name	C...	L...	Value	A...
FRO_OSC			30 MHz	
FRO oscillator			Power-up	
fro			fro clock is direct from FRO oscillator	
UART0 clock select			FRO	
UARTCLK0 Frequency			30 MHz	
UART0 clock			30 MHz	

Problems

type filter text

Level	Resource	Issue	Origin
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LPC845_Project_UH_tavmero

NXP LPC845* (LPC845 Pr...H tavmero)

18:03 2023. 02. 19.

Belső periféria funkció (jel) kőlső lábhoz rendelése a Pin perspektívában lehetséges. Példa: a CTIMER0 CAP0 bemenetének PIO16 lábhoz rendelése. Létrehozhatunk egy új jelcsoportot, ha az eddigi jelcsoportokba nem sorolható a jel: Pins/Functional Groups

UH_tavmero - LPC845_Project_UH_tavmero/board/pin_mux.c - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Pins Run Window Help

LPC845_Project_UH_tavmero

Functional Groups
Automatic Routing
Apply Expansion Board
Create Default Routing
Refresh
Reset to Board Defaults
Reset to Processor Defaults Ctrl+R

Pins Peripheral Signals External User Signals

Pin Pin name Label Identifier Breako

9	CAPT_X0	S1/PIO0_31/CAP...	CAPX	CN1[11]
3	CAPT_YH	S1/PIO1_9/CAPT...	CAPY_HIGH	
1	CAPT_YL	S1/PIO1_8/CAPT...	CAPY_LOW	
42	ACMP_I5	S1/PIO0_30/AC...	CAPY_R	CN1[16]
29	VDD29	VDD	CPU_VDD	
15	PIO0_16	CN1[11]/PIO0_16	CTIMER_CAP0	CN1[1]
20	PIO0_24	CN1[10]/PIO0_24	DEBUG_UART_RX	CN1[10] (TXI
19	PIO0_25	CN1[11]/PIO0_25	DEBUG_UART_TX	CN1[11] (RXI
4	PIO0_12	K1/CN1[25]/PIO...	K1	CN1[25] (ISP
5	RESETN/PIO0_5	K2/CN3[10]/CN1...	K2	CN1[9] (RES
6	PIO0_4/ADC_11	K3/CN1[35]/PIO...	K3	CN1[35] (USI
14	PIO1_1/CAPT_X2	LD1[4]/CN1[30]/...	LED_BLUE	CN1[30] (BLI
11	PIO1_0/CAPT_X1	LD1[3]/CN1[31]/...	LED_GREEN	CN1[31] (GR
16	PIO1_2/CAPT_X3	LD1[2]/CN1[29]/...	LED_RED	CN1[29] (REI
40	VDDA	FB1/VDDA	VDDA	

CAPY_LOW
CAPY_HIGH
K1
K2
K3

ACMP ADC0 CAPT CTIMER0
DAC0 DAC1 DMA0 GPIO
I2C0 I2C1 I2C2 I2C3
PINT SCT0 SPI0 SPI1
SUPPLY SWD SYSCON USART0
USART1 USART2 USART3 USART4
WKT

CPU_VT

Routing Details

Pins Signals type filter text

Routing Details for BOARD_Ini... 4

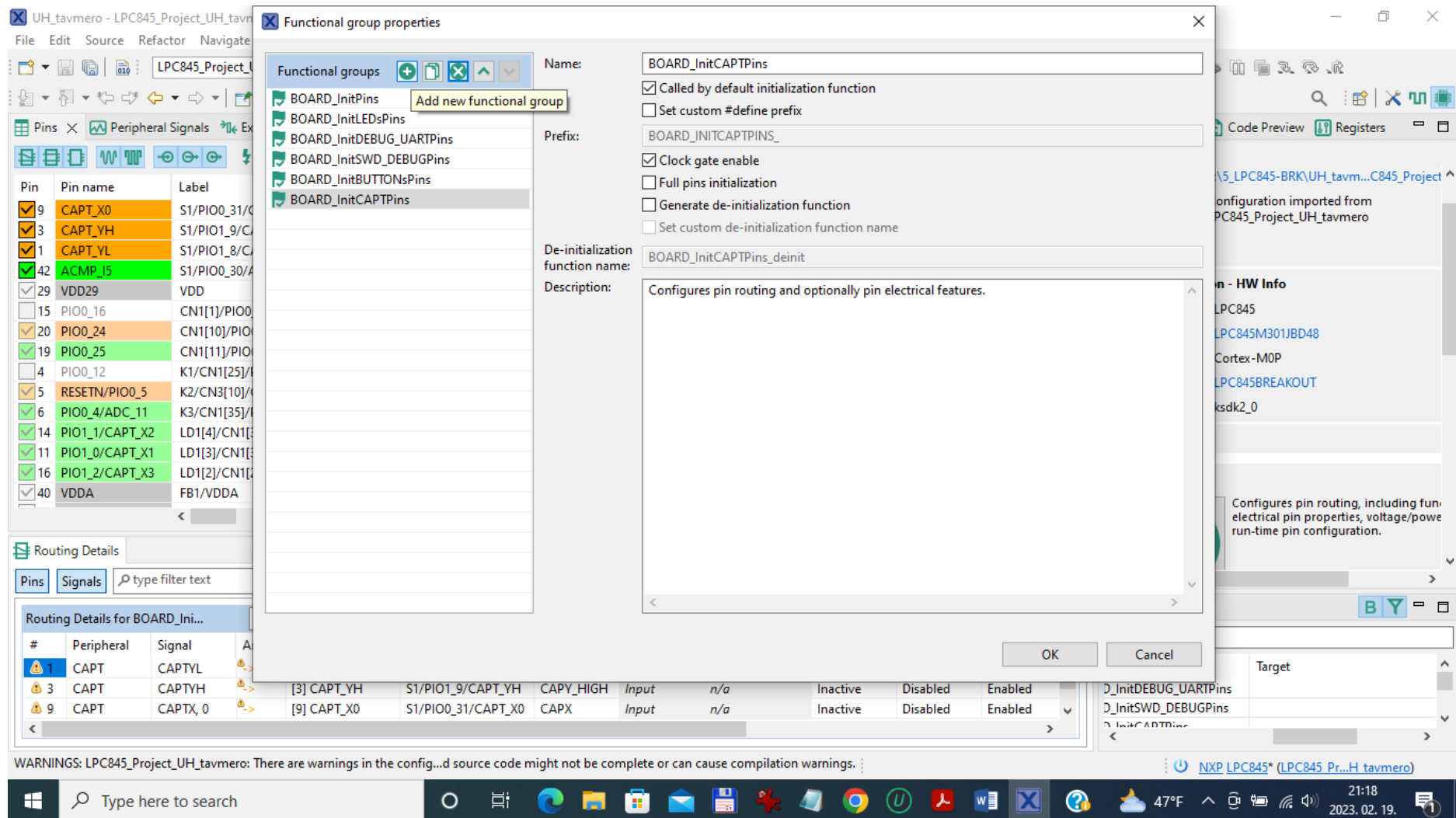
#	Peripheral	Signal	Arrow	Routed pin/signal	Label	Identifier	Direction	GPIO initial state	Mode	Invert	Hysteresis
1	CAPT	CAPT_YL	➔	[1] CAPT_YL	S1/PIO1_8/CAPT_YL	CAPY_LOW	Input	n/a	Inactive	Disabled	Enabled
3	CAPT	CAPT_YH	➔	[3] CAPT_YH	S1/PIO1_9/CAPT_YH	CAPY_HIGH	Input	n/a	Inactive	Disabled	Enabled
9	CAPT	CAPT_X0	➔	[9] CAPT_X0	S1/PIO0_31/CAPT_X0	CAPX	Input	n/a	Inactive	Disabled	Enabled

WARNINGS: LPC845_Project_UH_tavmero: There are warnings in the config...d source code might not be complete or can cause compilation warnings.

NXP LPC845* (LPC845 Pr...H tavmero)

21:15
2023. 02. 19.

A Functionla groups melleti + gombra klikkelünk.



UH_tavmero - LPC845_Project_UH_tavm

File Edit Source Refactor Navigate

LPC845_Project_UH_tavm

Pins X Peripheral Signals

Pin Pin name Label

Pin	Pin name	Label
9	CAPT_X0	S1/PIO0_31/C
3	CAPT_YH	S1/PIO1_9/C
1	CAPT_YL	S1/PIO1_8/C
42	ACMP_I5	S1/PIO0_30/A
29	VDD29	VDD
15	PIO0_16	CN1[1]/PIO
20	PIO0_24	CN1[10]/PIO
19	PIO0_25	CN1[11]/PIO
4	PIO0_12	K1/CN1[25]/
5	RESETN/PIO0_5	K2/CN3[10]/
6	PIO0_4/ADC_11	K3/CN1[35]/
14	PIO1_1/CAPT_X2	LD1[4]/CN1[
11	PIO1_0/CAPT_X1	LD1[3]/CN1[
16	PIO1_2/CAPT_X3	LD1[2]/CN1[
40	VDDA	FB1/VDDA

Routing Details

Pins Signals type filter text

Routing Details for BOARD_Ini...

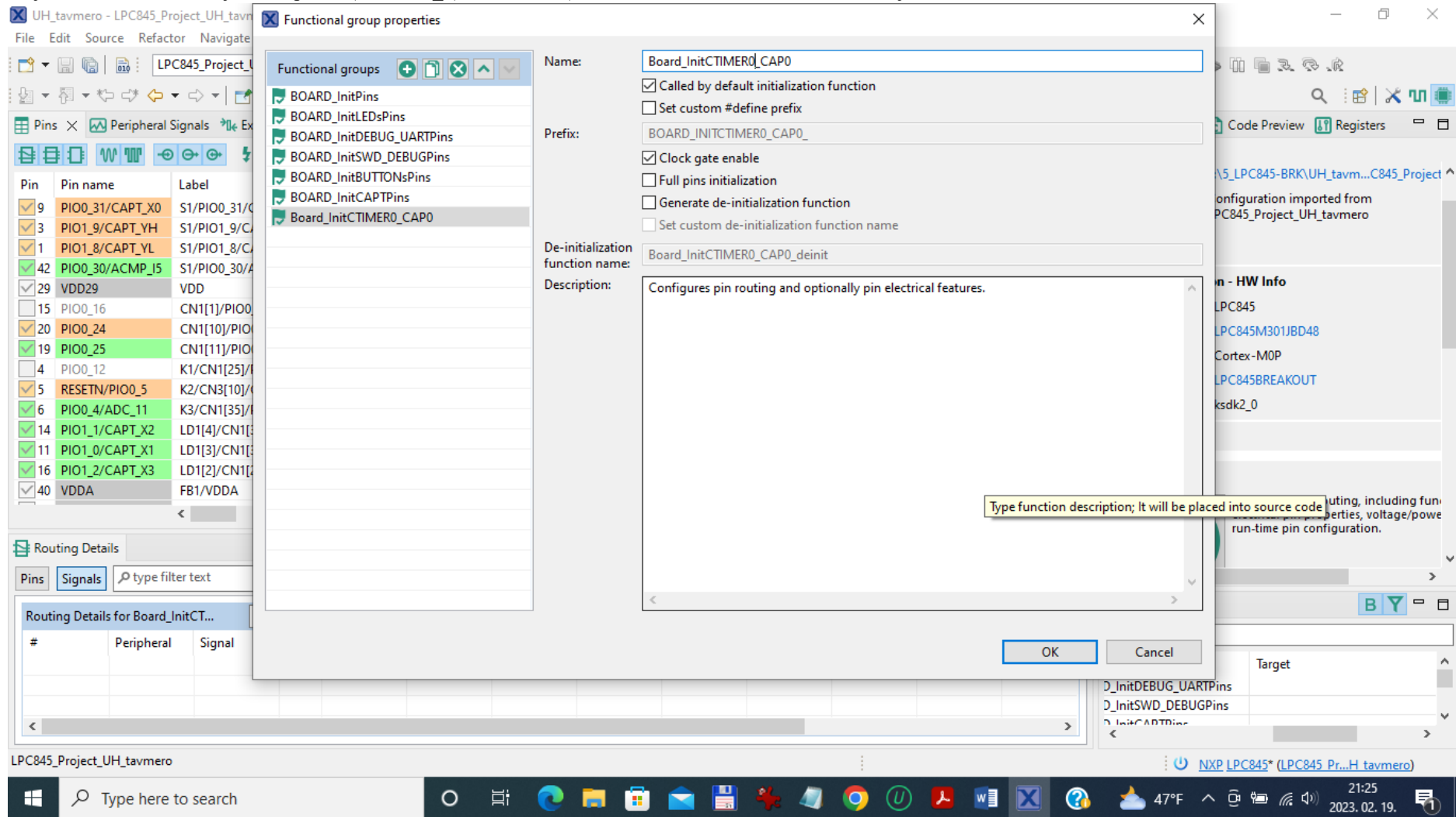
#	Peripheral	Signal	A
1	CAPT	CAPTYL	
3	CAPT	CAPTYH	[3] CAPT_YH S1/PIO1_9/CAPT_YH CAPY_HIGH Input n/a Inactive Disabled Enabled
9	CAPT	CAPT_X0	[9] CAPT_X0 S1/PIO0_31/CAPT_X0 CAPX Input n/a Inactive Disabled Enabled

WARNINGS: LPC845_Project_UH_tavmero: There are warnings in the config...d source code might not be complete or can cause compilation warnings.

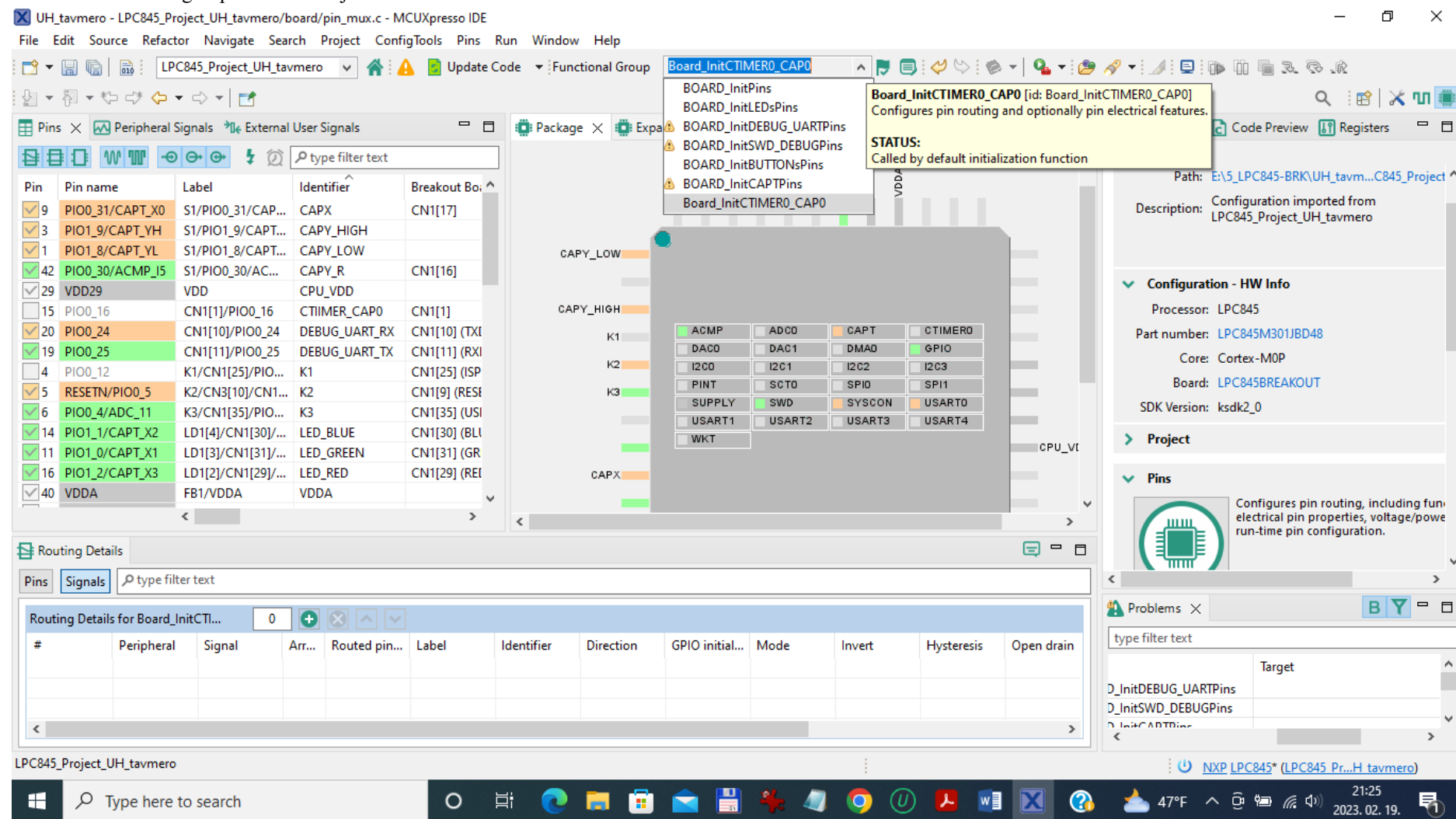
NXP LPC845* (LPC845 Pr...H_tavmero)

21:18 2023. 02. 19.

Majd a default néven létrejövő csoportot (PinsFunc_1) átnevezzük (itt Board_InitCTIMER0_CAP0-ra), majd OK.



Ezután a Functional group-nál kiválasztjuk.



A chipen ráklikkelünk a CTIMER0-ra.

UH_tavmero - LPC845_Project_UH_tavmero/board/pin_mux.c - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Pins Run Window Help

LPC845_Project_UH_tavmero Update Code Functional Group Board_InitCTIMER0_CAP0

Pins Peripheral Signals External User Signals

type filter text

Pin	Pin name	Label	Identifier	Breakout Bo...
<input checked="" type="checkbox"/>	9	PIO0_31/CAPT_X0	S1/PIO0_31/CAP...	CAPX
<input checked="" type="checkbox"/>	3	PIO1_9/CAPT_YH	S1/PIO1_9/CAPT...	CAPY_HIGH
<input checked="" type="checkbox"/>	1	PIO1_8/CAPT_YL	S1/PIO1_8/CAPT...	CAPY_LOW
<input checked="" type="checkbox"/>	42	PIO0_30/ACMP_I5	S1/PIO0_30/AC...	CAPY_R
<input checked="" type="checkbox"/>	29	VDD29	VDD	CPU_VDD
<input type="checkbox"/>	15	PIO0_16	CN1[1]/PIO0_16	CTIMER_CAP0
<input checked="" type="checkbox"/>	20	PIO0_24	CN1[10]/PIO0_24	DEBUG_UART_RX
<input checked="" type="checkbox"/>	19	PIO0_25	CN1[11]/PIO0_25	DEBUG_UART_TX
<input type="checkbox"/>	4	PIO0_12	K1/CN1[25]/PIO...	K1
<input checked="" type="checkbox"/>	5	RESETN/PIO0_5	K2/CN3[10]/CN1...	K2
<input checked="" type="checkbox"/>	6	PIO0_4/ADC_11	K3/CN1[35]/PIO...	K3
<input checked="" type="checkbox"/>	14	PIO1_1/CAPT_X2	LD1[4]/CN1[30]/...	LED_BLUE
<input checked="" type="checkbox"/>	11	PIO1_0/CAPT_X1	LD1[3]/CN1[31]/...	LED_GREEN
<input checked="" type="checkbox"/>	16	PIO1_2/CAPT_X3	LD1[2]/CN1[29]/...	LED_RED
<input checked="" type="checkbox"/>	40	VDDA	FB1/VDDA	VDDA

Package Expansion Header

CAPY_LOW

CAPY_HIGH

K1

K2

K3

CAPX

CAPY_R

VSSA

VDDA

CPU_VI

ACMP

DAC0

I2C0

PINT

SUPPLY

WKT

ADC0

DAC1

I2C1

SCT0

SWD

USART1

CAPT

DMA0

I2C2

SPI0

SYSCON

USART2

CTIMER0

USART3

USART4

Routing Details

Pins Signals

type filter text

Routing Details for Board_InitCTI... 0

#	Peripheral	Signal	Arr...	Routed pin...	Label	Identifier	Direction	GPIO initial...	Mode	Invert	Hysteresis	Open drain
---	------------	--------	--------	---------------	-------	------------	-----------	-----------------	------	--------	------------	------------

Overview Code Preview Registers

Path: E:\5_LPC845-BRK\UH_tavm...C845_Project

Description: Configuration imported from LPC845_Project_UH_tavmero

Configuration - HW Info

Processor: LPC845

Part number: LPC845M301JBD48

Core: Cortex-M0P

Board: LPC845BREAKOUT

SDK Version: ksdk2_0

Project

Pins

Configures pin routing, including fun... electrical pin properties, voltage/powe... run-time pin configuration.

Problems

type filter text

	Target
D_InitDEBUG_UARTPins	
D_InitSWD_DEBUGPins	
D_InitCAPTPins	

LPC845_Project_UH_tavmero

Type here to search

NXP LPC845* (LPC845 Pr...H_tavmero)

21:26

2023. 02. 19.

A felugró ablakban kiválasztjuk a lábra kivezetendő jelet (itt CAPTURE 0).

UH_tavmero - LPC845_Project_UH_tavmero/board/pin_mux.c - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Pins Run Window Help

LPC845_Project_UH_tavmero Update Code Functional Group Board_InitCTIMER0_CAP0

Pins Peripheral Signals External User Signals

Pin Pin name Label Identifier Breakout Bo...

Pin	Pin name	Label	Identifier	Breakout Bo...
9	PIO0_31/CAPT_X0	S1/PIO0_31/CAP...	CAPX	CN1[17]
3	PIO1_9/CAPT_YH	S1/PIO1_9/CAPT...	CAPY_HIGH	
1	PIO1_8/CAPT_YL	S1/PIO1_8/CAPT...	CAPY_LOW	
42	PIO0_30/ACMP_I5	S1/PIO0_30/AC...	CAPY_R	CN1[16]
29	VDD29	VDD	CPU_VDD	
15	PIO0_16	CN1[1]/PIO0_16	CTIMER_CAP0	CN1[1]
20	PIO0_24	CN1[10]/PIO0_24	DEBUG_UART_RX	CN1[10] (TXI
19	PIO0_25	CN1[11]/PIO0_25	DEBUG_UART_TX	CN1[11] (RXI
4	PIO0_12	K1/CN1[25]/PIO...	K1	CN1[25] (ISP
5	RESETN/PIO0_5	K2/CN3[10]/CN1...	K2	CN1[9] (RES
6	PIO0_4/ADC_11	K3/CN1[35]/PIO...	K3	CN1[35] (USI
14	PIO1_1/CAPT_X2	LD1[4]/CN1[30]/...	LED_BLUE	CN1[30] (BLI
11	PIO1_0/CAPT_X1	LD1[3]/CN1[31]/...	LED_GREEN	CN1[31] (GR
16	PIO1_2/CAPT_X3	LD1[2]/CN1[29]/...	LED_RED	CN1[29] (REI
40	VDDA	FB1/VDDA	VDDA	

Routing Details

Pins Signals

Routing Details for Board_InitCTI... 0

#	Peripheral	Signal	Arr...	Routed pin...	Label	Identifier	Direction	GPIO initial...	Mode	Invert	Hysteresis	Open drain
---	------------	--------	--------	---------------	-------	------------	-----------	-----------------	------	--------	------------	------------

LPC845_Project_UH_tavmero

Peripheral CTIMER0

All CTIMER0 signals for routing:

- ☐ CAPTURE, 0 - routable to 42 pins, 1 signal
- ☒ Signal CAPTURE, 0
- ☐ Capture input; Digital; Input
- ☐ Default (after reset) routing of pins and signal routes:
- ☐ Disabled
- ☐ MATCH, 1 - routable to 42 pins, 1 signal
- ☐ MATCH, 2 - routable to 42 pins, 1 signal
- ☐ MATCH, 3 - routable to 42 pins, 1 signal

Route All Unroute All

Make sure pin/signal assignment is correct in the Routing Details view.

Done

Overview Code Preview Registers

Path: E:\5_LPC845-BRK\UH_tavm...C845_Project

Description: Configuration imported from LPC845_Project_UH_tavmero

Configuration - HW Info

Processor: LPC845

Part number: LPC845M301JBD48

Core: Cortex-M0P

Board: LPC845BREAKOUT

SDK Version: ksdk2_0

Project

Pins

Configures pin routing, including fun... electrical pin properties, voltage/powe... run-time pin configuration.

Problems

type filter text

Target

D_InitDEBUG_UARTPins

D_InitSWD_DEBUGPins

D_InitCAPT...

NXP LPC845* (LPC845_P...H_tavmero)

21:27

2023. 02. 19.

The screenshot displays the MCUXpresso IDE interface for the LPC845 project. The 'Signal CTIM...' dialog is open, showing a list of routable pins. The 'Routing Details' table at the bottom provides a summary of the selected pin route.

Signal CTIM... Dialog - Routable pins / signal routes:

- ☐ [2] PIO0_13/ADC_10
- ☐ [37] PIO0_14/ACMP_13/ADC_2
- ☐ [22] PIO0_15
- ☒ [15] PIO0_16
- ☐ [48] PIO0_17/ADC_9/DACOUT_0
- ☐ [47] PIO0_18/ADC_8
- ☐ [46] PIO0_19/ADC_7
- ☐ [45] PIO0_20/ADC_6
- ☐ [44] PIO0_21/ADC_5
- ☐ [43] PIO0_22/ADC_4
- ☐ [39] PIO0_23/ADC_3/ACMP_14

Routing Details Table:

#	Peripheral	Signal	Arr...	Routed pin...	Label	Identifier	Direction	GPIO initial...	Mode	Invert	Hysteresis	Open drain
0	CTIMER0	CAP0		PIO0_16	CTIMER0_CAP0	CTIMER0_CAP0	Output	0	Push-Pull	False	False	False

Majd Done, Done. Ekkor az alsó Routing Details sorban megjelenik.

UH_tavmero - LPC845_Project_UH_tavmero/board/pin_mux.c - MCUXpresso IDE

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LPC845_Project_UH_tavmero

Update Code

Functional Group

Board_InitCTIMER0_CAP0

Pins

Peripheral Signals

External User Signals

type filter text

Pin	Pin name	Label	Identifier	Breakout Bo
9	PIO0_31/CAPT_X0	S1/PIO0_31/CAP...	CAPX	CN1[17]
3	PIO1_9/CAPT_YH	S1/PIO1_9/CAPT...	CAPY_HIGH	
1	PIO1_8/CAPT_YL	S1/PIO1_8/CAPT...	CAPY_LOW	
42	PIO0_30/ACMP_I5	S1/PIO0_30/AC...	CAPY_R	CN1[16]
29	VDD29	VDD	CPU_VDD	
15	PIO0_16	CN1[1]/PIO0_16	CTIMER_CAP0;...	CN1[1]
20	PIO0_24	CN1[10]/PIO0_24	DEBUG_UART_RX	CN1[10] (TXI
19	PIO0_25	CN1[11]/PIO0_25	DEBUG_UART_TX	CN1[11] (RXI
4	PIO0_12	K1/CN1[25]/PIO...	K1	CN1[25] (ISP
5	RESETN/PIO0_5	K2/CN3[10]/CN1...	K2	CN1[9] (RES
6	PIO0_4/ADC_11	K3/CN1[35]/PIO...	K3	CN1[35] (USI
14	PIO1_1/CAPT_X2	LD1[4]/CN1[30]/...	LED_BLUE	CN1[30] (BLI
11	PIO1_0/CAPT_X1	LD1[3]/CN1[31]/...	LED_GREEN	CN1[31] (GR
16	PIO1_2/CAPT_X3	LD1[2]/CN1[29]/...	LED_RED	CN1[29] (REI
40	VDDA	FB1/VDDA	VDDA	

Package

Signal CTIM...

Route All

Make sure p

Details view.

Routing Details

Pins

Signals

type filter text

Routing Details for Board_InitCTI... 1

#	Peripheral	Signal	Arrow	Routed pin/signal	Label	Identifier	Direction	GPIO initial state	Mode	Invert	Hysteresis	Op
15	CTIMER0	CAPTURE, 0	<-	[15] PIO0_16	CN1[1]/PIO0_16	Not Specified	Input	n/a	PullUp	Disabled	Enabled	Di

Overview

Code Preview

Registers

Path: E:\5_LPC845-BRK\UH_tavm...C845_Project

Description: Configuration imported from LPC845_Project_UH_tavmero

Configuration - HW Info

Processor: LPC845

Part number: LPC845M301JBD48

Core: Cortex-M0P

Board: LPC845BREAKOUT

SDK Version: ksdk2_0

Project

Pins

Configures pin routing, including fun... electrical pin properties, voltage/powe... run-time pin configuration.

Problems

type filter text

Target

D_InitDEBUG_UARTPins

D_InitSWD_DEBUGPins

D_InitCAPT...

LPC845_Project_UH_tavmero

Type here to search

21:46 2023. 02. 19.

. Ott módosíthatjuk a paramétereit, ha kell. (Most csak az Identifier-t írjuk be CTIMER0_CAP0, a többi jó mert ez egy Input).

UH_tavmero - LPC845_Project_UH_tavmero/board/pin_mux.c - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Pins Run Window Help

LPC845_Project_UH_tavmero Board_InitCTIMER0_CAP0

Package Expansion Header

Pin Pin name Label Identifier Breakout Bo...

Pin	Pin name	Label	Identifier	Breakout Bo...
9	PIO0_31/CAPT_X0	S1/PIO0_31/CAP...	CAPX	CN1[17]
3	PIO1_9/CAPT_YH	S1/PIO1_9/CAPT...	CAPY_HIGH	
1	PIO1_8/CAPT_YL	S1/PIO1_8/CAPT...	CAPY_LOW	
42	PIO0_30/ACMP_I5	S1/PIO0_30/AC...	CAPY_R	CN1[16]
29	VDD29	VDD	CPU_VDD	
15	PIO0_16	CN1[1]/PIO0_16	CTIMER0_CAP0...	CN1[1]
20	PIO0_24	CN1[10]/PIO0_24	DEBUG_UART_RX	CN1[10] (TXI...
19	PIO0_25	CN1[11]/PIO0_25	DEBUG_UART_TX	CN1[11] (RXI...
4	PIO0_12	K1/CN1[25]/PIO...	K1	CN1[25] (ISP...
5	RESETN/PIO0_5	K2/CN3[10]/CN1...	K2	CN1[9] (RES...
6	PIO0_4/ADC_11	K3/CN1[35]/PIO...	K3	CN1[35] (USI...
14	PIO1_1/CAPT_X2	LD1[4]/CN1[30]/...	LED_BLUE	CN1[30] (BLI...
11	PIO1_0/CAPT_X1	LD1[3]/CN1[31]/...	LED_GREEN	CN1[31] (GR...
16	PIO1_2/CAPT_X3	LD1[2]/CN1[29]/...	LED_RED	CN1[29] (REI...
40	VDDA	FB1/VDDA	VDDA	

Routing Details

Pins Signals type filter text

Routing Details for Board_InitCTI... 1

#	Peripheral	Signal	Arrow	Routed pin/signal	Label	Identifier	Direction	GPIO initial state	Mode	Invert	Hysteresis	Op...
15	CTIMER0	CAPTURE_0	<-	[15] PIO0_16	CN1[1]/PIO0_16	CTIMER0_CAP0	Input	n/a	PullUp	Disabled	Enabled	Di...

LPC845_Project_UH_tavmero

NXP LPC845* (LPC845 Pr...H tavmero)

21:47 2023.02.19.

Path: E:\5_LPC845-BRK\UH_tavm...C845_Project

Description: Configuration imported from LPC845_Project_UH_tavmero

Configuration - HW Info

Processor: LPC845

Part number: LPC845M301BD48

Core: Cortex-M0P

Board: LPC845BREAKOUT

SDK Version: ksd2_0

Project

Pins

Configures pin routing, including fun... electrical pin properties, voltage/powe... run-time pin configuration.

Problems

type filter text

Target

D_InitDEBUG_UARTPins

D_InitSWD_DEBUGPins

D_InitCAPT...

Most a már meglévő BOARD_InitPins jelcsoporthoz adunk kimenet portot, hogy a paraméter változtatást is megmutassuk. Ehhez a chipben a GPIO perifériára klikkelünk, majd kiválasztjuk a PIO0_18 portot és Done.

The screenshot shows the MCUXpresso IDE interface for the LPC845 project. The 'Peripheral GPIO' window is open, displaying a list of all GPIO signals for routing. The signal 'PIO0_18 - [47] PIO0_18/ADC_8; default' is selected. The 'Route All' button is visible. Below the window, the 'Routing Details' table shows the configuration for the selected signal.

#	Peripheral	Signal	Arr...	Routed pin...	Label	Identifier	Direction	GPIO initial...	Mode	Invert	Hysteresis	Open drain
47	GPIO	PIO0_18	-	[47] PIO0_18	CN1[3]/PI...	n/a	Not Specifi...	n/a	PullUp	Disabled	Enabled	Disabled

The background shows the project overview with the path 'E:\5_LPC845-BRK\UH_tavm...C845_Project' and the description 'Configuration imported from LPC845_Project_UH_tavmero'. The 'Configuration - HW Info' section lists the processor as LPC845, part number as LPC845M301JBD48, core as Cortex-M0P, board as LPC845BREAKOUT, and SDK version as ksd2_0. The 'Pins' section indicates that the configuration includes pin routing, functional pin properties, voltage/power, and run-time pin configuration.

Most a Routing Details-ben a port irányát beállítjuk Output-nak, az azonosítót megadjuk (UH_TRIG), a kezdőértéke marad Logical 0. Ezután Update code.

UH_tavmero - LPC845_Project_UH_tavmero/board/pin_mux.c - MCUXpresso IDE

File Edit Source Refactor Navigate Search Project ConfigTools Pins Run Window Help

LPC845_Project_UH_tavmero Update Code Functional Group BOARD_InitPins

Pins Peripheral Signals External User Signals

Pin Pin name Label Identifier Breakout Bo

9	PIO0_31/CAPT_X0	S1/PIO0_31/CAP...	CAPX	CN1[17]
3	PIO1_9/CAPT_YH	S1/PIO1_9/CAPT...	CAPY_HIGH	
1	PIO1_8/CAPT_YL	S1/PIO1_8/CAPT...	CAPY_LOW	
42	PIO0_30/ACMP_I5	S1/PIO0_30/AC...	CAPY_R	CN1[16]
29	VDD29	VDD	CPU_VDD	
15	PIO0_16	CN1[11]/PIO0_16	CTIMER_CAP0;...	CN1[1]
20	PIO0_24	CN1[10]/PIO0_24	DEBUG_UART_RX	CN1[10] (TXI
19	PIO0_25	CN1[11]/PIO0_25	DEBUG_UART_TX	CN1[11] (RXI
4	PIO0_12	K1/CN1[25]/PIO...	K1	CN1[25] (ISP
5	RESETN/PIO0_5	K2/CN3[10]/CN1...	K2	CN1[9] (RES
6	PIO0_4/ADC_11	K3/CN1[35]/PIO...	K3	CN1[35] (USI
14	PIO1_1/CAPT_X2	LD1[4]/CN1[30]/...	LED_BLUE	CN1[30] (BLI
11	PIO1_0/CAPT_X1	LD1[3]/CN1[31]/...	LED_GREEN	CN1[31] (GR
16	PIO1_2/CAPT_X3	LD1[2]/CN1[29]/...	LED_RED	CN1[29] (REI
47	PIO0_18	CN1[3]/PIO0_18/...	UH_TRIG	CN1[3]

Routing Details

Pins Signals type filter text

Routing Details for BOARD_InitPins 1

#	Peripheral	Signal	Arrow	Routed pin/signal	Label	Identifier	Direction	GPIO initial state	Mode	Invert	Hysteresis
47	GPIO	PIO0_18	->	[47] PIO0_18	CN1[3]/PIO0_18/ADC_8	UH_TRIG	Output	Logical 0	PullUp	Disabled	Enabled

LPC845_Project_UH_tavmero

Overview Code Preview Registers

Path: E:\5_LPC845-BRK\UH_tavm...C845_Project

Description: Configuration imported from LPC845_Project_UH_tavmero

Configuration - HW Info

Processor: LPC845

Part number: LPC845M301JBD48

Core: Cortex-M0P

Board: LPC845BREAKOUT

SDK Version: ksd2_0

Project

Pins

Configures pin routing, including fun...
electrical pin properties, voltage/powe...
run-time pin configuration.

Problems

type filter text

Target

D_InitDEBUG_UARTPins

D_InitSWD_DEBUGPins

D_InitCAPT...

NXP LPC845* (LPC845 Pr...H_tavmero)

21:50
2023. 02. 19.