



Europass Curriculum Vitae

Personal information

Surname(s) / First name(s) **Pechan Imre**
Address(es) 19 Tartsay street, H-1126 Budapest
Telephone(s) +361-356-6413 **Mobile:** +3630-514-3604
E-mail pechanimre@freemail.hu
Nationality Hungarian
Date of birth 22.04.1985
Gender Male

Work experience

Dates June – July 2007
Occupation or position held Summer intern
Main activities and responsibilities Automatic code generation with Matlab RTW to Xilinx Virtex PowerPC platform
Name and address of employer Thyssenkrupp Presta R&D Budapest, 1-3 Daróci street, H-1113 Budapest
Type of business or sector Automotive industry

Education and training

Dates 2003 - 2008
Date of graduation June 2008
Title of qualification M.Sc. in Electrical Engineering
Principal subjects/occupational skills covered Embedded systems
 FPGA programming
 Digital signal processing
Name and type of organisation providing education and training Budapest University of Technology and Economics, Faculty of Electrical Engineering and Informatics

Personal skills and competences

Mother tongue(s) **Hungarian**

Other language(s)

Self-assessment
European level ()*

English

German

Language exam certificate(s)

Understanding				Speaking				Writing	
Listening		Reading		Spoken interaction		Spoken production			
B2	Independent User	B2	Independent User	B2	Independent User	B2	Independent User	B2	Independent User
A2	Basic User	A2	Basic User	A2	Basic User	A2	Basic User	A2	Basic User

Intermediate level state accredited C type English language exam (examination centre: BME Nyelvvizsgaközpont)
 Intermediate level state accredited C type German language exam (examination centre: Goethe-Institut Budapest)

Other skills and competences	Programming skills: C, C++, Java, LabVIEW Hardware description languages: Verilog Acquaintance with Xilinx FPGAs, Atmel microcontrollers and Texas DSPs Acquaintance with database management systems (Oracle, MSSQL) Good command of Matlab
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